

Hello all,
i have 32F072BDISCOVERY with STM32F072RBT6.

Soldet external 8mhz crystal,

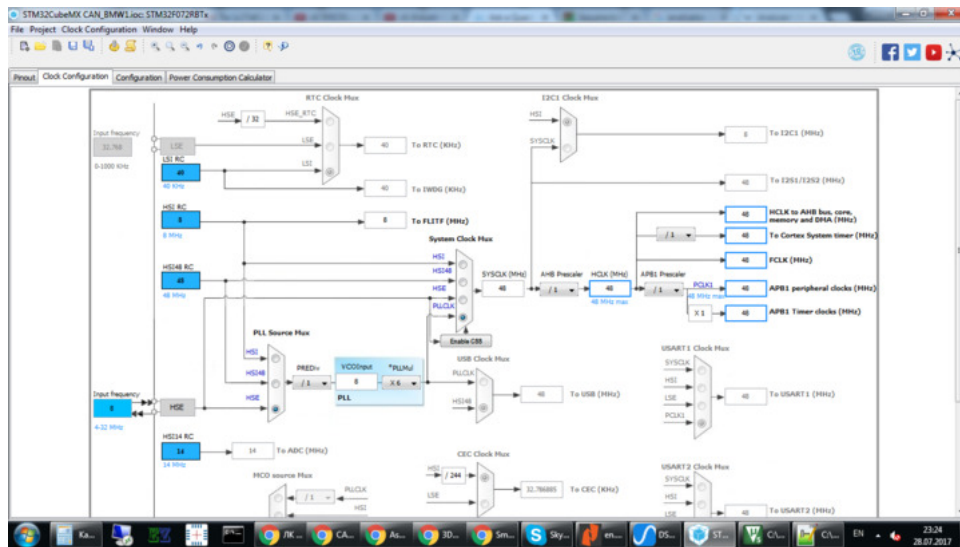
Remove SB17, SB18, SB1
X3 (8mhz crystal), R36, R37, C22, C23 soldered

The problem is that can bus not working, i trying to check it by Analyser and have connected mp 2551, no signal....

Configured all by cubemx with default pins 11 and 12pa.

HAL

Before soldet crystal and after, the same problem, no signal.



CAN Configuration

Parameter Settings User Constants NVIC Settings GPIO Settings

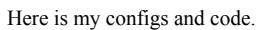
Search Signals
Search (Ctrl+F)

Show only Modified Pins

Pin Name	Signal on Pin	GPIO output...	GPIO mode	GPIO Pull-up...	Maximum o...	Fast Mode	User Label	Modified
PA11	CAN_RX	n/a	Alternate Fu...	No pull-up a...	High	n/a		
PA12	CAN_TX	n/a	Alternate Fu...	No pull-up a...	High	n/a		

Select Pins from table to configure them. Multiple selection is Allowed.

Restore Default Apply OK Cancel



```

/*****
* File Name      : main.c
* Description    : Main program body
*****/
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* OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.
*****/
*/
/* Includes -----*/
#include "main.h"
#include "stm32f0xx_hal.h"

/* USER CODE BEGIN Includes */

/* USER CODE END Includes */

/* Private variables -----*/
CAN_HandleTypeDef hcan;

/* USER CODE BEGIN PV */
/* Private variables -----*/
static CanTxMsgTypeDef      canTxMessage;
static CanRxMsgTypeDef      canRxMessage;
/* USER CODE END PV */

/* Private function prototypes -----*/
void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_CAN_Init(void);

/* USER CODE BEGIN PFP */
/* Private function prototypes -----*/
void CAN_Transmit(void);

/* USER CODE END PFP */

/* USER CODE BEGIN 0 */
uint16_t CAN_ID;
uint8_t  CAN_DLC;
uint8_t  DATA[8];

void CAN_Transmit(void)
{
    //????????? ?????????? ??? ??????????:

```

```

    hcan.pTxMsg->StdId = 0x222;
    hcan.pTxMsg->DLC = 8;
    hcan.pTxMsg->Data[0] = 1;
    hcan.pTxMsg->Data[1] = 2;
    hcan.pTxMsg->Data[2] = 3;
    hcan.pTxMsg->Data[3] = 4;
    hcan.pTxMsg->Data[4] = 5;
    hcan.pTxMsg->Data[5] = 6;
    hcan.pTxMsg->Data[6] = 7;
    hcan.pTxMsg->Data[7] = 8;
    HAL_CAN_Transmit(&hcan, 10);
}

void HAL_CAN_RxCpltCallback(CAN_HandleTypeDef* CanHandle)
{
    HAL_GPIO_TogglePin(GPIOC, GPIO_PIN_6);
    CAN_ID = CanHandle->pRxMsg->StdId;
    CAN_DLC = CanHandle->pRxMsg->DLC;
    DATA[0] = CanHandle->pRxMsg->Data[0];
    DATA[1] = CanHandle->pRxMsg->Data[1];
    DATA[2] = CanHandle->pRxMsg->Data[2];
    DATA[3] = CanHandle->pRxMsg->Data[3];
    DATA[4] = CanHandle->pRxMsg->Data[4];
    DATA[5] = CanHandle->pRxMsg->Data[5];
    DATA[6] = CanHandle->pRxMsg->Data[6];
    DATA[7] = CanHandle->pRxMsg->Data[7];
    HAL_CAN_Receive_IT(&hcan, CAN_FIFO0);
}

/* USER CODE END 0 */

int main(void)
{
    /* USER CODE BEGIN 1 */

    /* USER CODE END 1 */

    /* MCU Configuration-----*/

    /* Reset of all peripherals, Initializes the Flash interface and the Systick. */
    HAL_Init();

    /* USER CODE BEGIN Init */

    /* USER CODE END Init */

    /* Configure the system clock */
    SystemClock_Config();

    /* USER CODE BEGIN SysInit */

    /* USER CODE END SysInit */

    /* Initialize all configured peripherals */
    MX_GPIO_Init();
    MX_CAN_Init();

    /* USER CODE BEGIN 2 */

    hcan.pTxMsg = &canTxMessage;
    hcan.pRxMsg = &canRxMessage;

    CAN_FilterConfTypeDef canFilterConfig;
    canFilterConfig.FilterNumber = 0;
    canFilterConfig.FilterMode = CAN_FILTERMODE_IDLIST;
    canFilterConfig.FilterScale = CAN_FILTERSCALE_16BIT;
    canFilterConfig.FilterIdHigh = 0x53A<<5; //????? 1
    canFilterConfig.FilterIdLow = 0x54C<<5; //????? 2
    canFilterConfig.FilterMaskIdHigh = 0x53D<<5; //????? 3
    canFilterConfig.FilterMaskIdLow = 0x54F<<5; //????? 4
    canFilterConfig.FilterFIFOAssignment = 0;
    canFilterConfig.FilterActivation = ENABLE;
    canFilterConfig.BankNumber = 1;
    HAL_CAN_ConfigFilter(&hcan, &canFilterConfig);

    canFilterConfig.FilterNumber = 1;
    canFilterConfig.FilterIdHigh = 0xA<<5; //????? 5
    canFilterConfig.FilterIdLow = 0x33<<5; //????? 6
    canFilterConfig.FilterMaskIdHigh = 0xA<<5; //????? 7
    canFilterConfig.FilterMaskIdLow = 0x33<<5; //????? 8
    HAL_CAN_ConfigFilter(&hcan, &canFilterConfig);

    HAL_CAN_Receive_IT(&hcan, CAN_FIFO0);

    /* USER CODE END 2 */

    /* Infinite loop */
    /* USER CODE BEGIN WHILE */
    while (1)
    {
        /* USER CODE END WHILE */

        /* USER CODE BEGIN 3 */
        CAN_Transmit();
        HAL_Delay(200);
        HAL_GPIO_TogglePin (GPIOC, GPIO_PIN_8);
    }
    /* USER CODE END 3 */
}

```

```

/** System Clock Configuration
*/
void SystemClock_Config(void)
{

    RCC_OscInitTypeDef RCC_OscInitStruct;
    RCC_ClkInitTypeDef RCC_ClkInitStruct;

    /**Initializes the CPU, AHB and APB busses clocks
    */
    RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSE;
    RCC_OscInitStruct.HSEState = RCC_HSE_ON;
    RCC_OscInitStruct.HSIState = RCC_HSI_ON;
    RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
    RCC_OscInitStruct.PLL.PLLSource = RCC_PLLSOURCE_HSE;
    RCC_OscInitStruct.PLL.PLLMUL = RCC_PLL_MUL6;
    RCC_OscInitStruct.PLL.PREDIV = RCC_PREDIV_DIV1;
    if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
    {
        _Error_Handler(__FILE__, __LINE__);
    }

    /**Initializes the CPU, AHB and APB busses clocks
    */
    RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK|RCC_CLOCKTYPE_SYSCLK
                                |RCC_CLOCKTYPE_PCLK1;
    RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
    RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
    RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV1;

    if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_1) != HAL_OK)
    {
        _Error_Handler(__FILE__, __LINE__);
    }

    /**Configure the SysTick interrupt time
    */
    HAL_SYSTICK_Config(HAL_RCC_GetHCLKFreq()/1000);

    /**Configure the SysTick
    */
    HAL_SYSTICK_CLKSourceConfig(SYSTICK_CLKSOURCE_HCLK);

    /* SysTick_IRQn interrupt configuration */
    HAL_NVIC_SetPriority(SysTick_IRQn, 0, 0);
}

/* CAN init function */
static void MX_CAN_Init(void)
{
    hcan.Instance = CAN;
    hcan.Init.Prescaler = 20;
    hcan.Init.Mode = CAN_MODE_NORMAL;
    hcan.Init.SJW = CAN_SJW_1TQ;
    hcan.Init.BS1 = CAN_BS1_15TQ;
    hcan.Init.BS2 = CAN_BS2_8TQ;
    hcan.Init.TTCM = DISABLE;
    hcan.Init.ABOM = DISABLE;
    hcan.Init.AWUM = DISABLE;
    hcan.Init.NART = DISABLE;
    hcan.Init.RFLM = DISABLE;
    hcan.Init.TXFP = DISABLE;
    if (HAL_CAN_Init(&hcan) != HAL_OK)
    {
        _Error_Handler(__FILE__, __LINE__);
    }
}

/** Configure pins as
    * Analog
    * Input
    * Output
    * EVENT_OUT
    * EXTI
    */
static void MX_GPIO_Init(void)
{
    GPIO_InitTypeDef GPIO_InitStruct;

    /* GPIO Ports Clock Enable */
    __HAL_RCC_GPIOF_CLK_ENABLE();
    __HAL_RCC_GPIOC_CLK_ENABLE();
    __HAL_RCC_GPIOA_CLK_ENABLE();

    /*Configure GPIO pin Output Level */
    HAL_GPIO_WritePin(GPIOC, GPIO_PIN_6|GPIO_PIN_7|GPIO_PIN_8|GPIO_PIN_9, GPIO_PIN_RESET);

    /*Configure GPIO pins : PC6 PC7 PC8 PC9 */
    GPIO_InitStruct.Pin = GPIO_PIN_6|GPIO_PIN_7|GPIO_PIN_8|GPIO_PIN_9;
    GPIO_InitStruct.Mode = GPIO_MODE_OUTPUT_PP;
    GPIO_InitStruct.Pull = GPIO_NOPULL;
    GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_LOW;
    HAL_GPIO_Init(GPIOC, &GPIO_InitStruct);
}

/* USER CODE BEGIN 4 */

/* USER CODE END 4 */

```

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