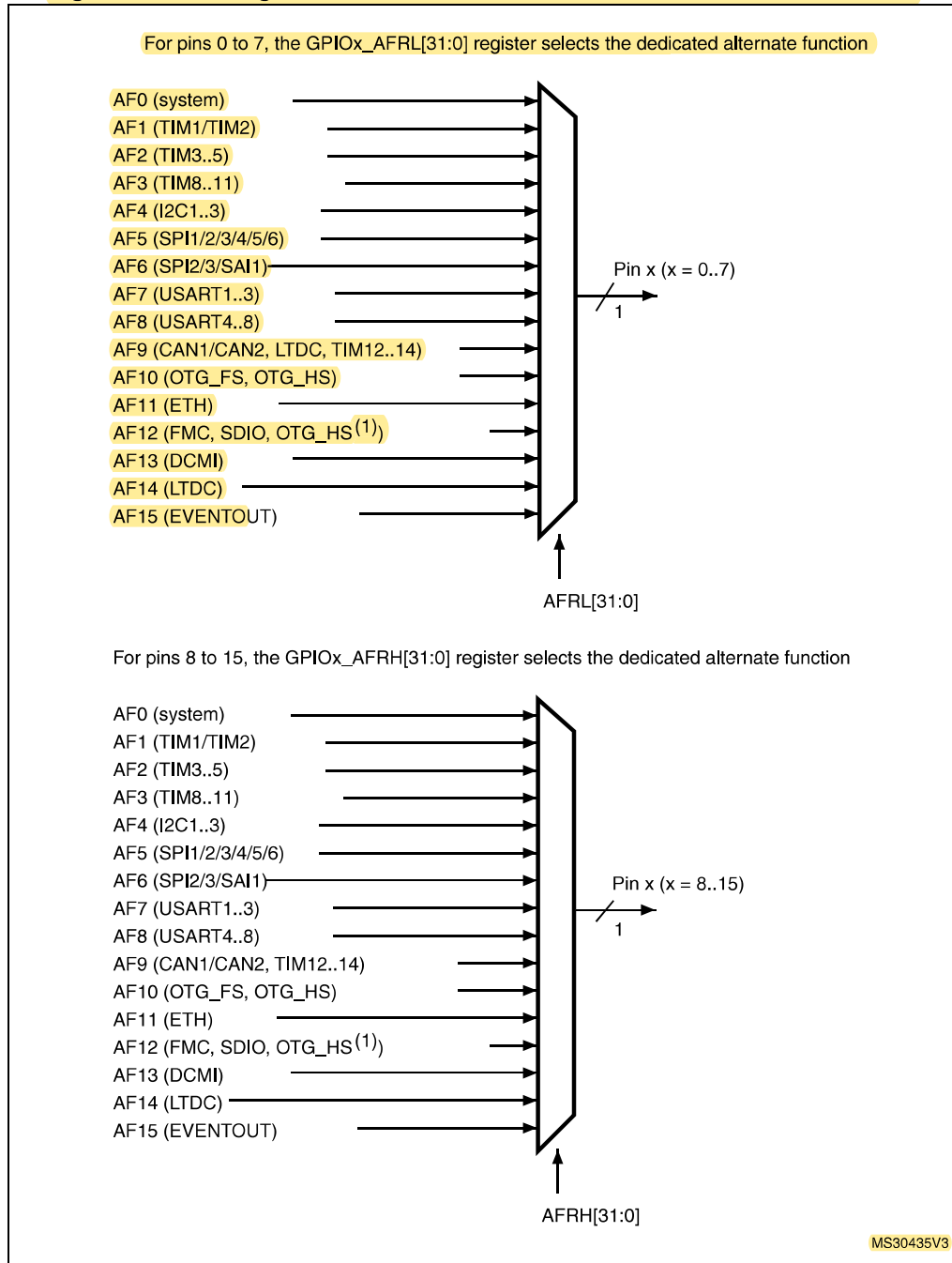


Figure 27. Selecting an alternate function on STM32F42xxx and STM32F43xxx



1. Configured in FS.