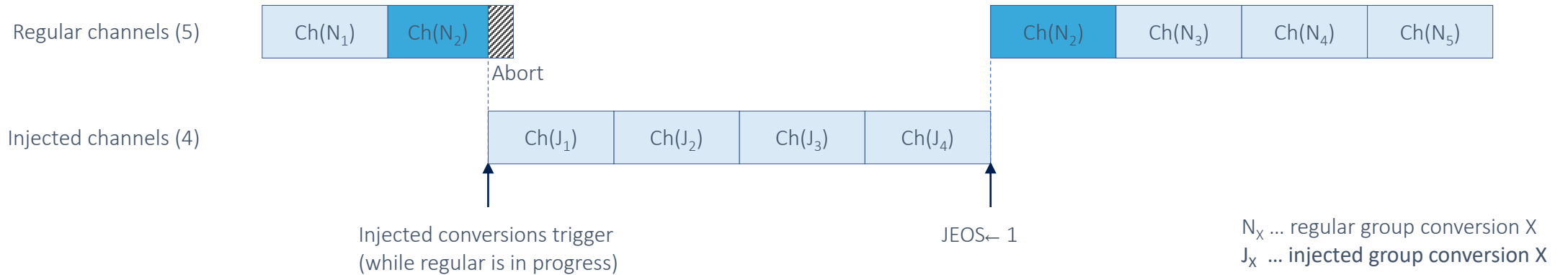
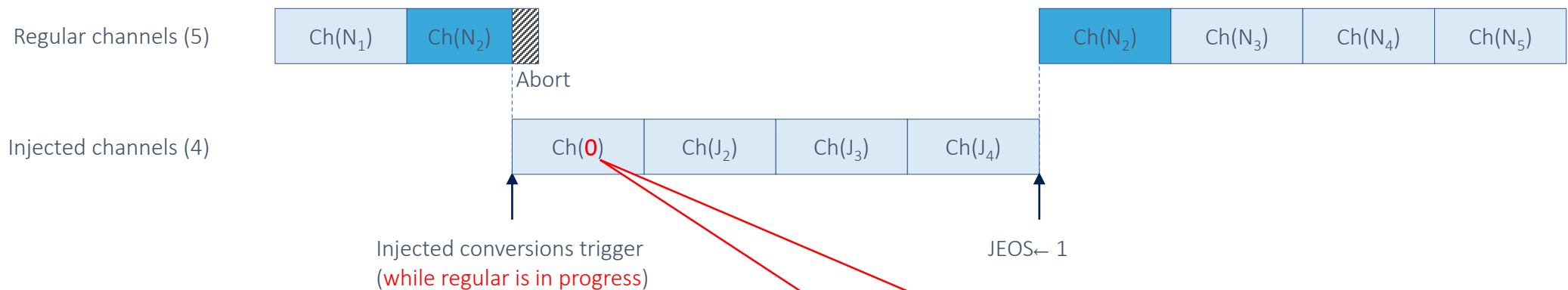


# Limitation description

## Interruption during of the ADC regular conversion – Normal behavior (F1, F3, F4..., G4 Rev Z):



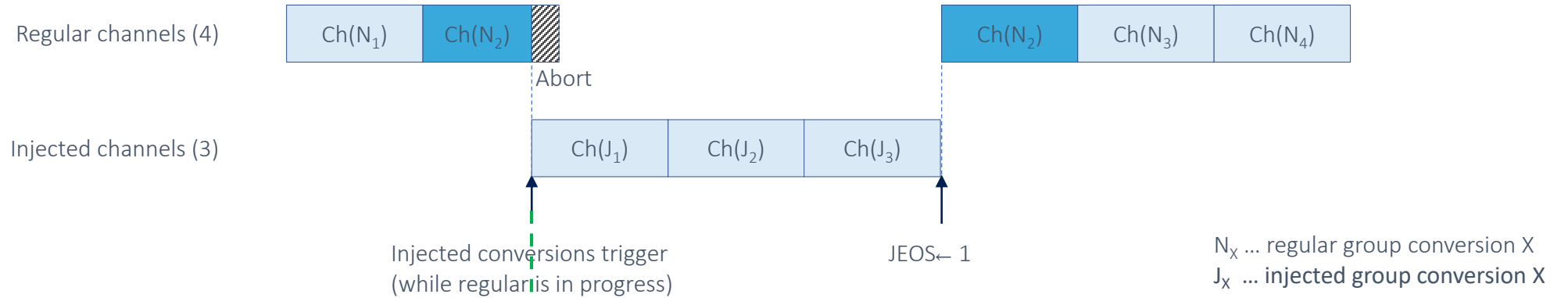
## Interruption during of the ADC regular conversion – incorrect behavior (G4 Rev Y)



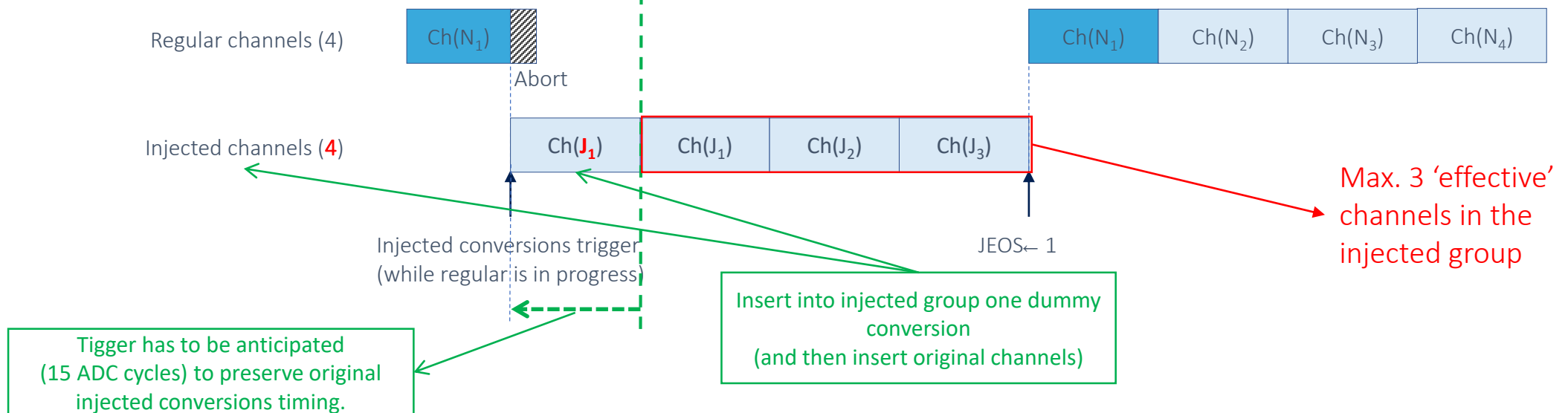
First injected group conversion is replaced by internal **Channel 0** (which is internally grounded).

# Workaround 1 - Explanation

## Interruption during of the ADC regular conversion – Normal behavior:

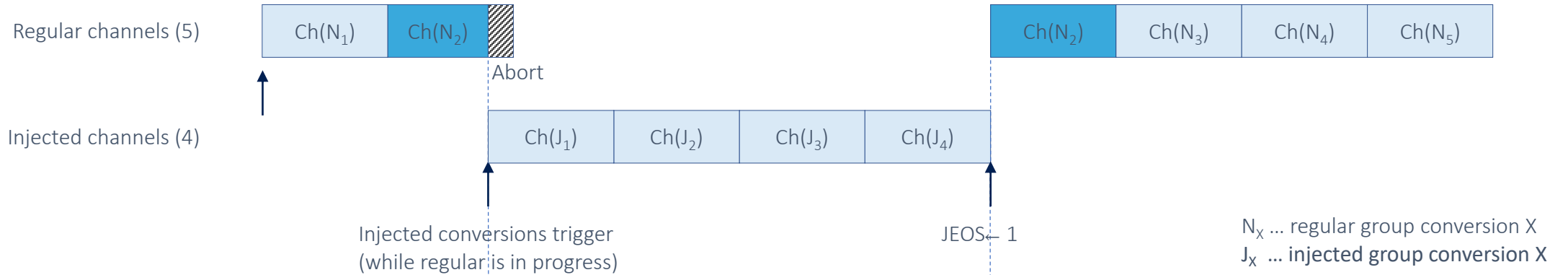


## Workaround 1: Inserting dummy channel into injected group



# Workaround 2 - Explanation

## Interruption during of the ADC regular conversion – Normal behavior:



## Workaround 2: Trigger regular conversion to avoid collision with injected conversion

