

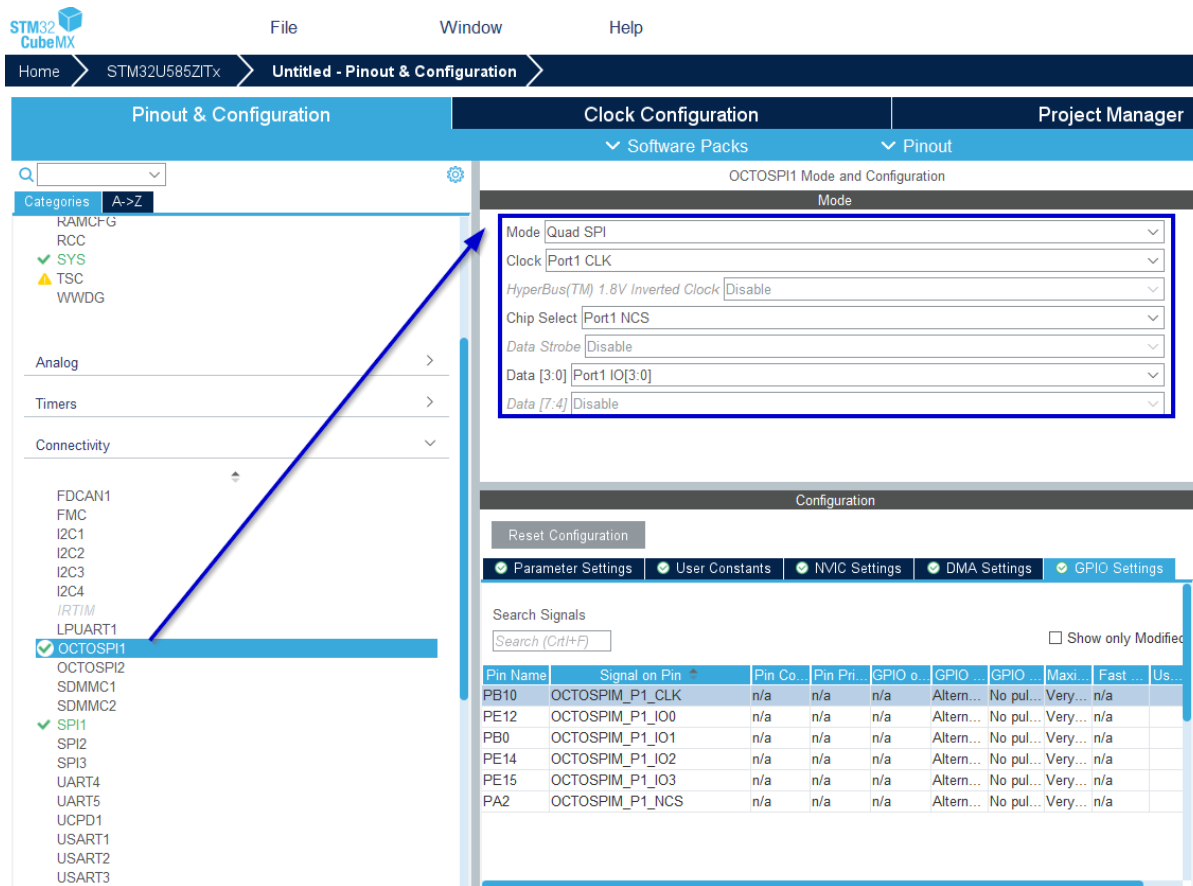


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**Example of**

**M95P32-I & STM32U5 interface**

# STM32U5 SPI interfaces



- **The STM32U5 has 2 SPI interfaces**

- Single SPI

- Manage only single SPI

- OctoSPI

- Manage single, dual, quad or octo SPI interface

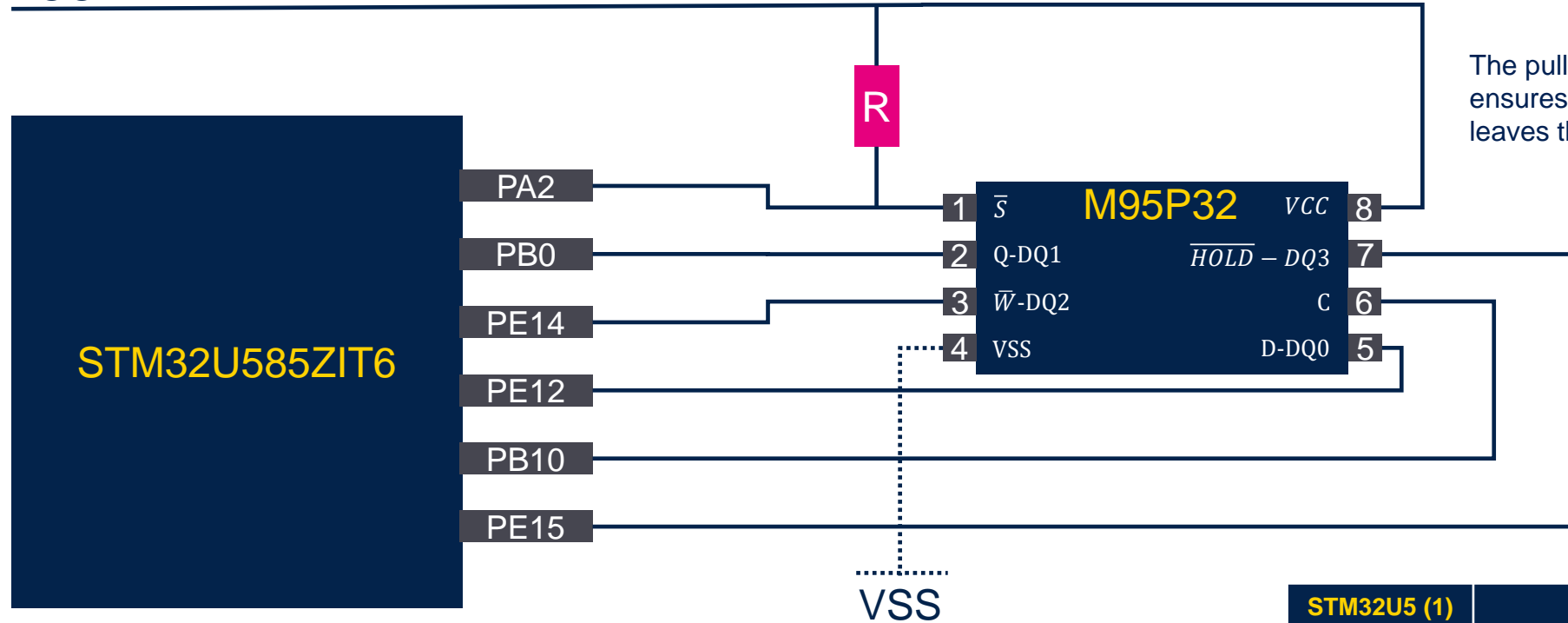
- **To have flexibility we will configure the OctoSPI as Quad SPI**

**In that configuration, the STM32U5 will be able to address the M95P32 in Single, Dual Or Quad SPI**



# STM32U5 & M95P32-I schematic

VCC



The pull-up resistor R (whose typical value is 100 kΩ) ensures that a device is not selected if the bus master leaves the S line in the high impedance state

STM32U585ZIT6

In this configuration,  $\overline{HOLD}$  and  $\overline{W}$  are driven by the Octo SPI macro

STM32U5 (1)	M95P32
PA2	Chip Select
PB0	Serial data output / Serial data output 1 for dual / quad
PE14	Write protect / Serial data output 2 for quad
PE12	Serial data input / Serial data output 0 for dual / quad
PB10	Serial clock
PE15	Hold / Serial data output 3 for quad

(1)These pin names are compatible with the Arduino Zio connector (CN10) of STM32 Nucleo boards

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