



Erasing & Programming

Download

File path:

Start addr...:

Skip flash erase before programming

Verify programming

Run after programming

Automatic Mode

Full chip erase

Download file

Option bytes commands:

Log

```
16:42:30:707 : Memory Programming ...
16:42:30:707 : Opening and parsing file: RAM_v93.bin
16:42:30:708 : File      : RAM_v93.bin
16:42:30:708 : Size       : 154.17 KB
16:42:30:708 : Address    : 0x02000000
16:42:30:708 : Download in Progress:
16:42:30:708 : Size      : 157868 Bytes
16:42:30:708 : Address   : 0x02000000
16:42:30:758 : Response received from device: NACK
16:42:30:758 : Error: Write Operation fails at address 0x20000000
16:42:30:758 : Error: failed to download Segment[0]
16:42:30:758 : Error: failed to download the File
```

```

/*###ICF### SDefine regionDefine region, don't touch! ****/
/*-Editor annotation file-*/
/* IcfEditorFile="$TOOLKIT_DIR$\config\ide\IcfEditor\cortex_v1_0.xml" */

/*-Specials-*/
define symbol __ICFEDIT_intvec_start__ = 0x20008000;
/*-Memory Regions-*/
define symbol __ICFEDIT_region_ROM_start__ = 0x0;
define symbol __ICFEDIT_region_ROM_end__ = 0x0;

define symbol __ICFEDIT_region_SRAM1_start__ = 0x20008000;
define symbol __ICFEDIT_region_SRAM1_end__ = 0x2002CFFF;
define symbol __ICFEDIT_region_LUTROM_start__ = 0x2002D000;
define symbol __ICFEDIT_region_LUTROM_end__ = 0x2002FFFF;

define symbol __ICFEDIT_region_SRAM2_start__ = 0x10000000;
define symbol __ICFEDIT_region_SRAM2_end__ = 0x1000FFFF;

/*-Sizes-*/
define symbol __ICFEDIT_size_cstack__ = 0x400;
define symbol __ICFEDIT_size_heap__ = 0x200;
/**** End of ICF editor section. ###ICF###*/

define memory mem with size = 4G;
define region ROM_region = mem:[from __ICFEDIT_region_ROM_start__ to __ICFEDIT_region_ROM_end__];
define region SRAM1_region = mem:[from __ICFEDIT_region_SRAM1_start__ to __ICFEDIT_region_SRAM1_end__];
define region SRAM2_region = mem:[from __ICFEDIT_region_SRAM2_start__ to __ICFEDIT_region_SRAM2_end__];

define block CSTACK with alignment = 8, size = __ICFEDIT_size_cstack__ { };
define block HEAP with alignment = 8, size = __ICFEDIT_size_heap__ { };

place at address mem:__ICFEDIT_intvec_start__ { readonly section .intvec };
export symbol __ICFEDIT_intvec_start__;

place in SRAM1_region { readonly };
place at end of SRAM1_region { section .checksum };

place at start of SRAM2_region { block CSTACK };
place in SRAM2_region { section .bss, readwrite, block HEAP };

place at address mem:__ICFEDIT_region_LUTROM_start__ { readonly section lut_section };

initialize manually with packing = none { readwrite };
do not initialize { section .bss,
                    section .noinit };

```