



- (1) TrustZone = disabled.
- (2) Updated RCC with external XTAL (same setting as eval board)
- (3) Updated PWR to use SMPS
- (4) Enable ICache.
- (5) Enable USART1, async.
- (6) Update Code, clean and build no error
- (7) Found it running on 4MHz system clock, so I went to clock, attached PPL to 16MHZ XTAL and then resolve clock issue so I get 160MHz
- (8) Update and I get this message. Could not see any yellow or red marker on RCC list and clock tree. I also get PRW as well as RCC.
- (9) I also took a demo code from STM library and generate code, it also reported RCC/PWR warning.

