

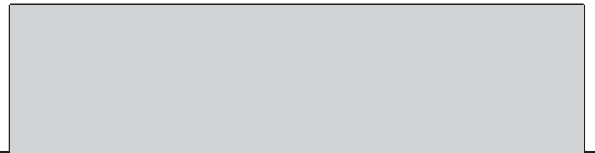
TIM1 ARR == combination of TIM2 ARR values



TIM1_CH1



TIM1_CH2

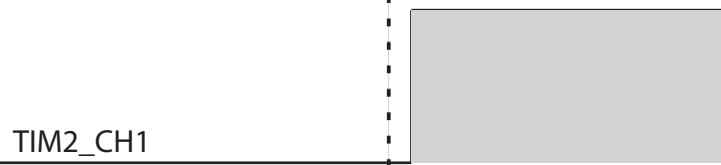


TIM1_CH3

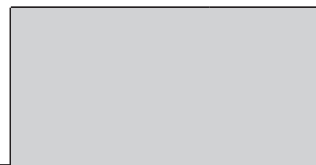


TIM1_CH4(NO Output)

Point of Alignment in time



TIM2_CH1



TIM2_CH2



TIM2_CH3



TIM2_CH4

TIM2 constantly changes ARR, and CCR values of the four channels on the fly by utilizing DMA-Burst feature there are 5 Bursts of Register update in any given TIM1 period, the arbitrary waves are produced correctly with respect to each other however they do not align with TIM1, the delays are off.