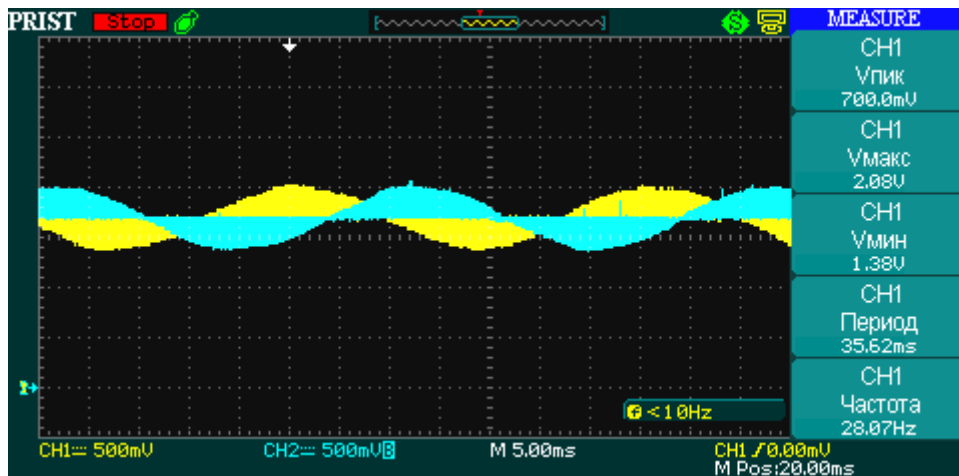
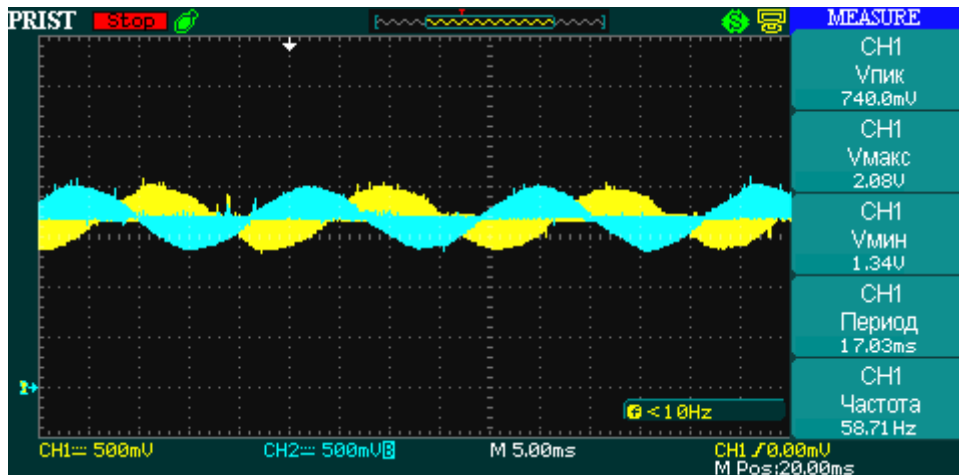


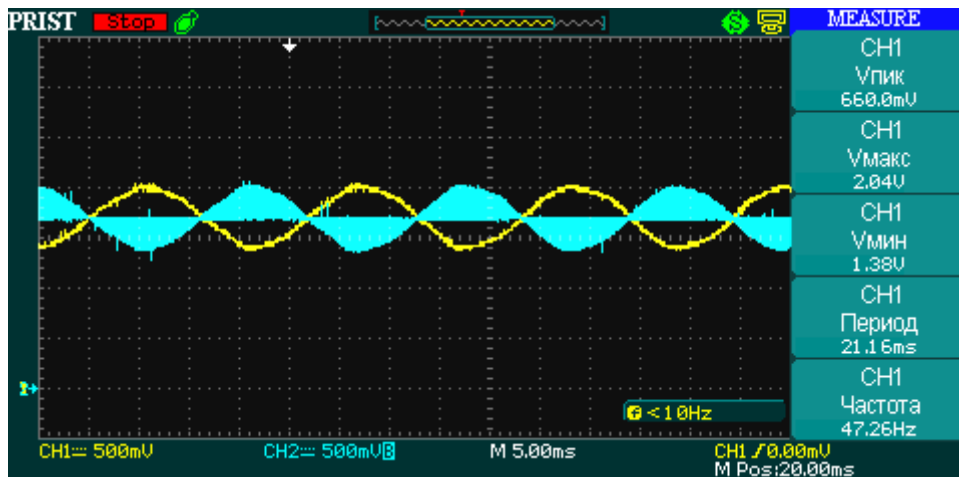
CH1 – out amplifier U, CH2 – out amplifier V



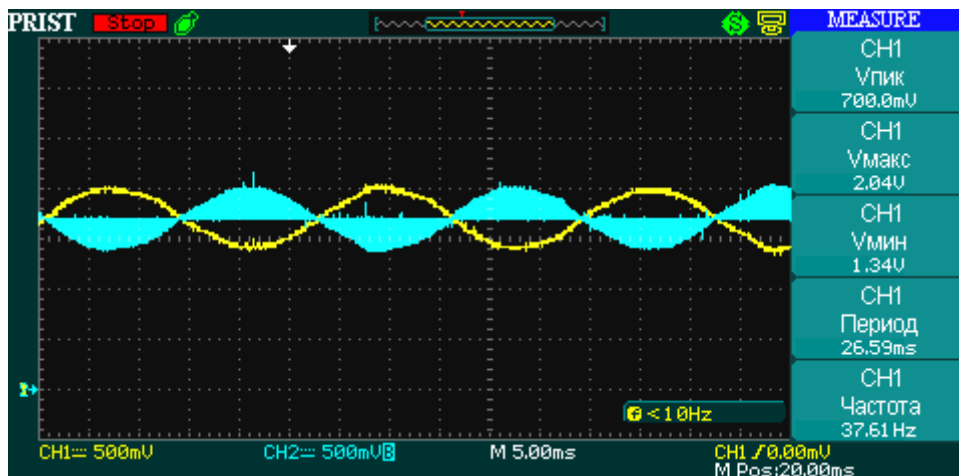
CH1 – out amplifier V, CH2 – out amplifier W



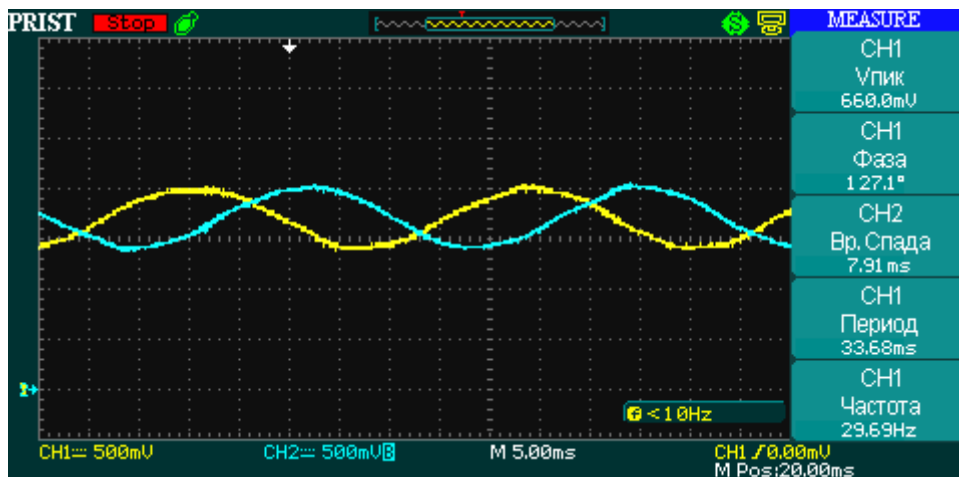
CH1 – out amplifier U, CH2 – out amplifier W



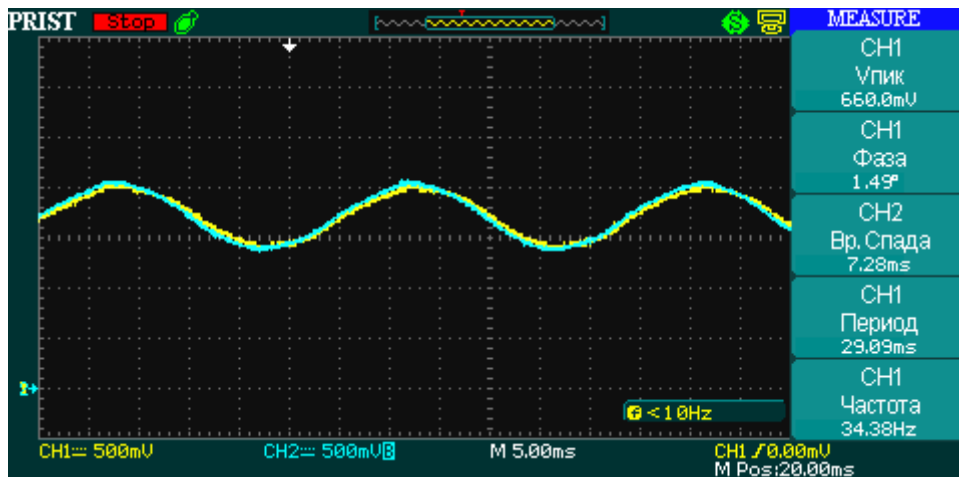
CH1 – Ia, CH2 – out amplifier U



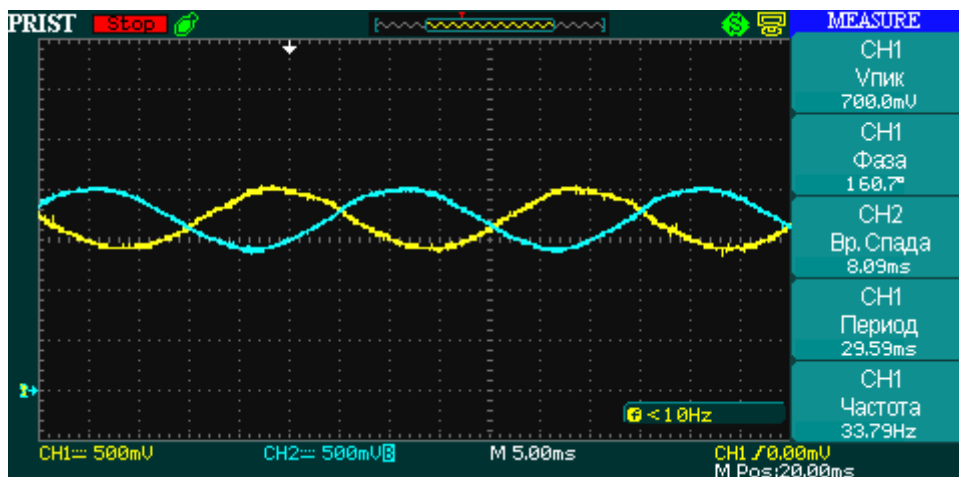
CH1 – Ib, CH2 – out amplifier V



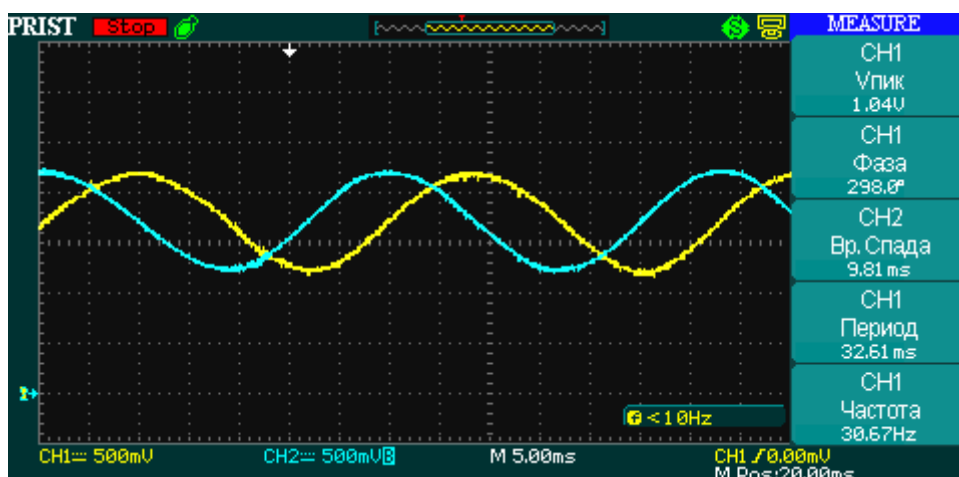
CH1 – Ia, CH2 – Ib



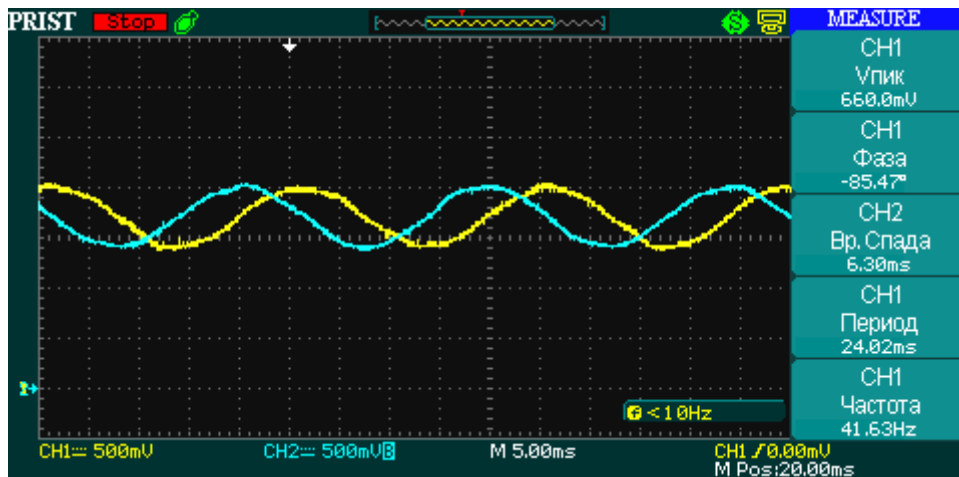
CH1 – Ia, CH2 – Ialpha Observer PLL



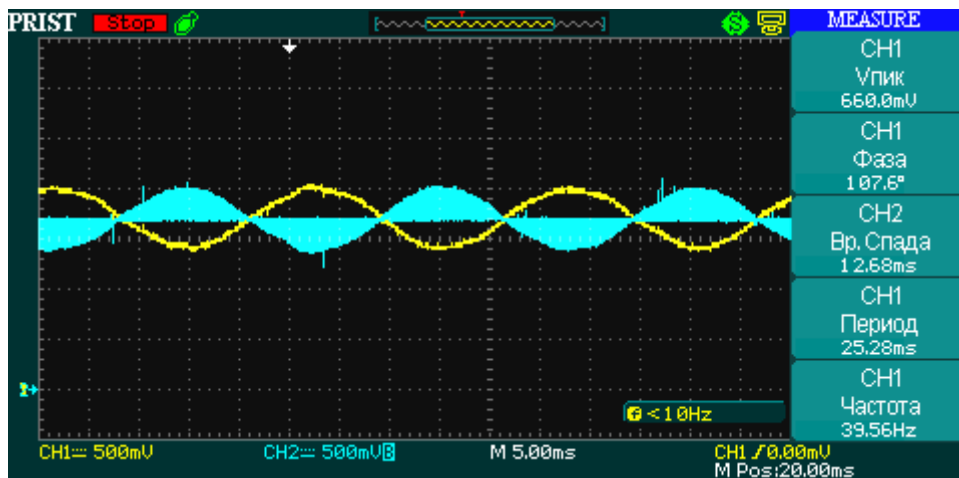
CH1 – Ib, CH2 – Ibeta Observer PLL



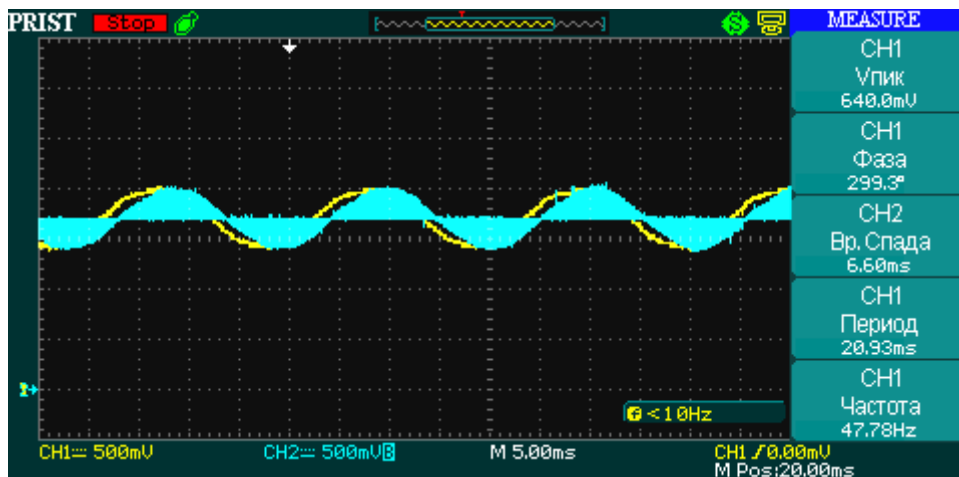
CH1 – Ialpha Observer PLL, CH2 – Ibeta Observer PLL



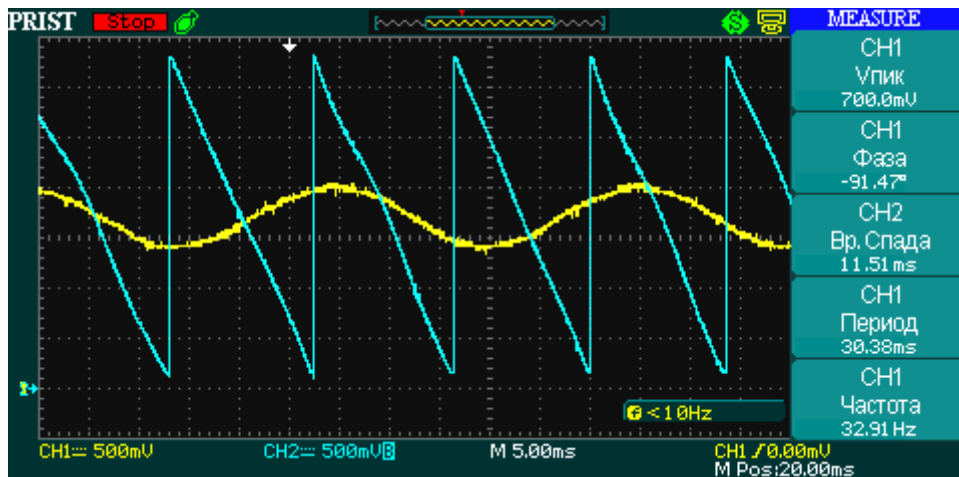
CH1 – Ialpha, CH2 – Ibeta



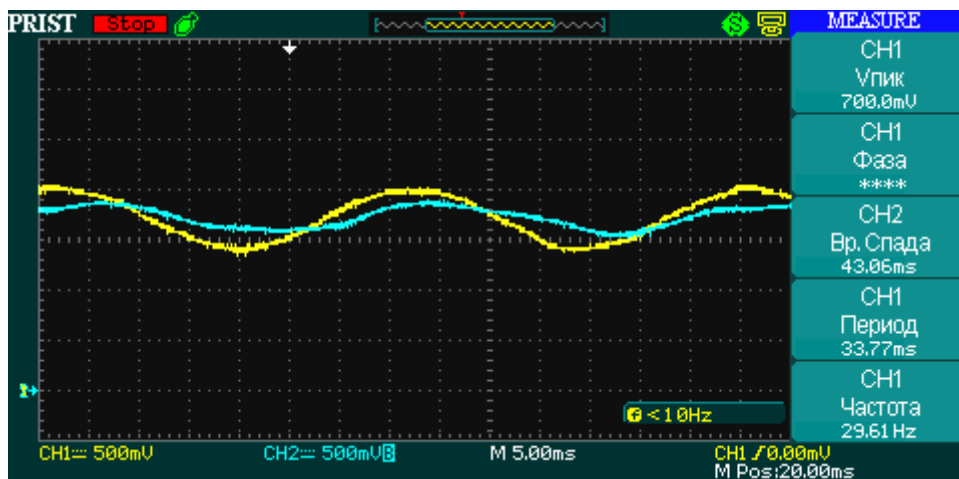
CH1– Ialpha Observer PLL, CH2– out amplifier U



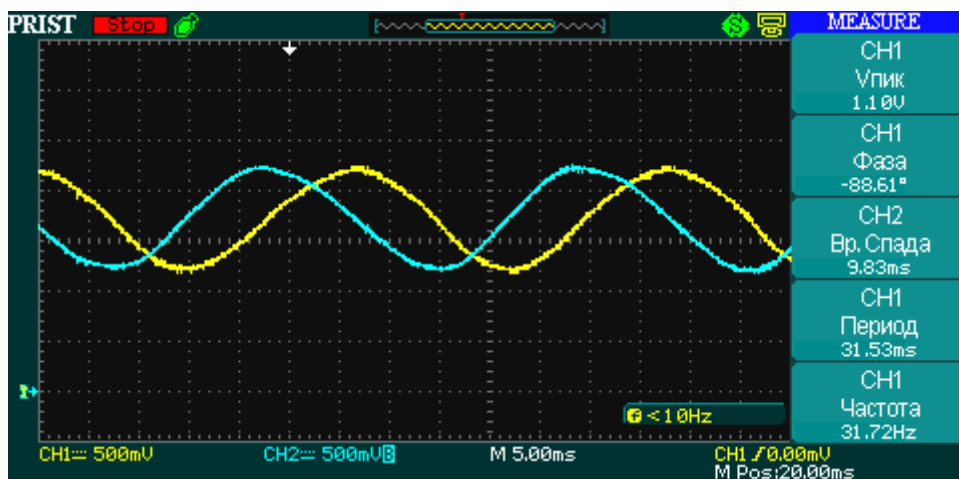
CH1 – Ibeta Observer PLL, CH2– out amplifier V



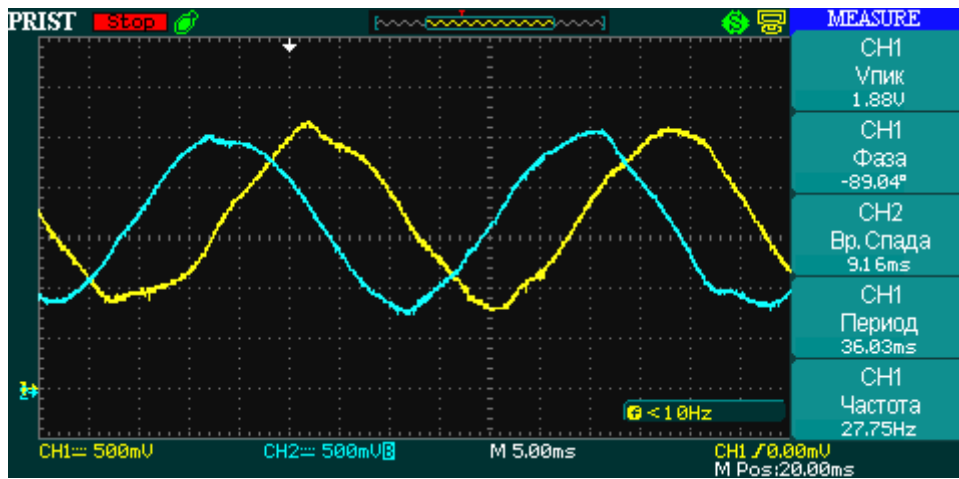
CH1 – Ialpha Observer PLL, CH2– Qrel (PLL)



CH1 – Ia, CH2– BEMF alpha Observer PLL



CH1 – BEMF alpha Observer PLL, CH2– BEMF beta Observer PLL



CH1 – V alpha, CH2– V beta