

Data exchange between STM32F411 and PC using USB

Note for using **CDC**(communication device class) or **VCP**(Virtual Com Port) to **exchange data between STM32 and PC via USB**.

The sample code and related tools can be downloaded via:

https://bitbucket.org/rwmao/cdc_onstm32f411rc/downloads

https://bitbucket.org/rwmao/cdc_onstm32f411rc/src it is a repository. There is wiki too.

If you are not patient to read through, you can directly go to the last part:

Bugs or possible error you may face.

Development tools:

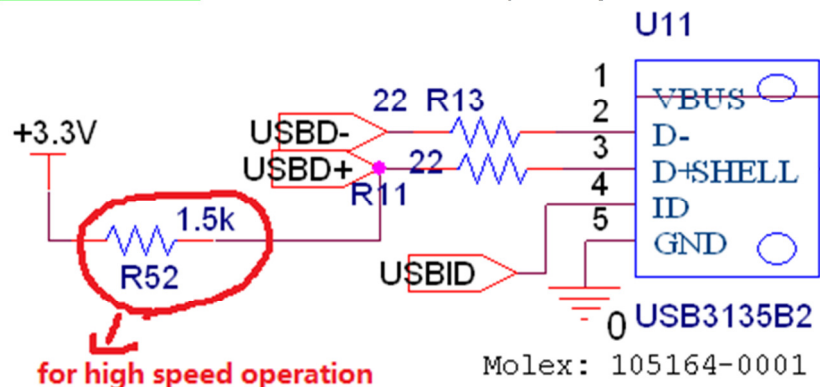
Keil 5.16a, FDP2.6 pack.

Cubemx v1.8pack.

Chip: STM32F411RC 64pins.

Implementation of hardware:

On STM32 side, USB must be correctly configured.



The d- and d+ datalines are simply connected to the STM32F411RC micro-controller (PA11 and PA12).

One of the most important thing is the 1.5k pull-up resistor to identify the devices itself as high speed USB.

Without the resistor, PC may not be able to detect the device at all.

On PC side, no need for any hardware implementation as long as USB port is available.

VCP driver is needed for PC to correctly access the data.

<http://www.st.com/web/en/catalog/tools/PF257938>

You also need a software to monitor the data sent through USB (virtual com port).

The software can be found in the repository.

2. Implement the USB TX function.

(1). Implement the data sending function, `CDC_Transmit_FS`, in file: `usbd_cdc_if.c`

```
/**
 * @brief CDC_Transmit_FS
 *
 * Data send over USB IN endpoint are sent over CDC interface
 * through this function.
 *
 * @note
 *
 * @param Buf: Buffer of data to be send
 *
 * @param Len: Number of data to be send (in bytes)
 *
 * @retval Result of the operation: USBD_OK if all operations are OK
 * else USBD_FAIL or USBD_BUSY
 */
uint8_t CDC_Transmit_FS(uint8_t* Buf, uint16_t Len)
{
    //I revised the code to send long strings exceeding
    APP_TX_DATA_SIZE. rwmao

    uint8_t result = USBD_OK;

    if ( hUsbDevice_0 == NULL ) return USBD_FAIL;

    USBD_CDC_HandleTypeDef *pCDC =
        (USBD_CDC_HandleTypeDef *)hUsbDevice_0->pClassData;

    if ( pCDC->TxState != 0 ) return USBD_BUSY;

    /* USER CODE BEGIN 8 */
    if (Len > APP_TX_DATA_SIZE)
    {
        int offset;
```

```

    for (offset = 0; offset < Len; offset++)
    {
        int todo = MIN(APP_TX_DATA_SIZE,
                       Len - offset);
        result = CDC_Transmit_FS(Buf + offset, todo);
        if ( ( result != USBD_OK ) && ( result != USBD_BUSY )
) {
            /* Error: Break out now */
            return result;
        }
    }
    return USBD_OK;
}

pCDC = (USBDCDC_HandleTypeDef *)hUsbDevice_0->pClassData;
/* TODO: Consider a timeout in the following wait loop. */
while(pCDC->TxState) { } //Wait for previous transfer to complete

int i;
for ( i = 0; i < Len; i++ ) {
    UserTxBufferFS[i] = Buf[i];
}
USBDCDC_SetTxBuffer(hUsbDevice_0, &UserTxBufferFS[0], Len);
result = USBDCDC_TransmitPacket(hUsbDevice_0);

/* USER CODE END 8 */
return result;
}

```

You can change the buffer size too.

```

/* USER CODE BEGIN 1 */
/* Define size for the receive and transmit buffer over CDC */
/* It's up to user to redefine and/or remove those define */
#define APP_RX_DATA_SIZE 64
#define APP_TX_DATA_SIZE 64
/* USER CODE END 1 */

```

Now the function to send data is ready.

For convenience, we define a function to call the subroutine in **usb_device.c(.h)**.

```

51
52 US[C:\Users\aa\Desktop\ARMProjects\CDC_STM32F411_cubemx\Src\usb_device.c]
53
54 USBDC_RegisterInterface(&hUsbDeviceFS, &USBDC_Interface_fops_FS);
55
56 USBDC_Start(&hUsbDeviceFS);
57
58 }
59
60 void MX_USB_DEVICE_SENT_DATA(uint8_t* Buf, uint16_t Len)
61 {
62     CDC_Transmit_FS(Buf, Len);
63 }
64

```

```

40 /* Includes -----
41 #include "stm32f4xx.h"
42 #include "stm32f4xx_hal.h"
43 #include "usbd_def.h"
44
45 extern USBDC_HandleTypeDef hUsbDeviceFS;
46
47 /* USB_Device init function */
48 void MX_USB_DEVICE_Init(void);
49 void MX_USB_DEVICE_SENT_DATA(uint8_t* Buf, uint16_t Len);
50
51 #ifdef __cplusplus
52 -}
53 #endif
54 #endif /* usb_device.h */

```

Now in main.c, you can call the subroutine to send data.

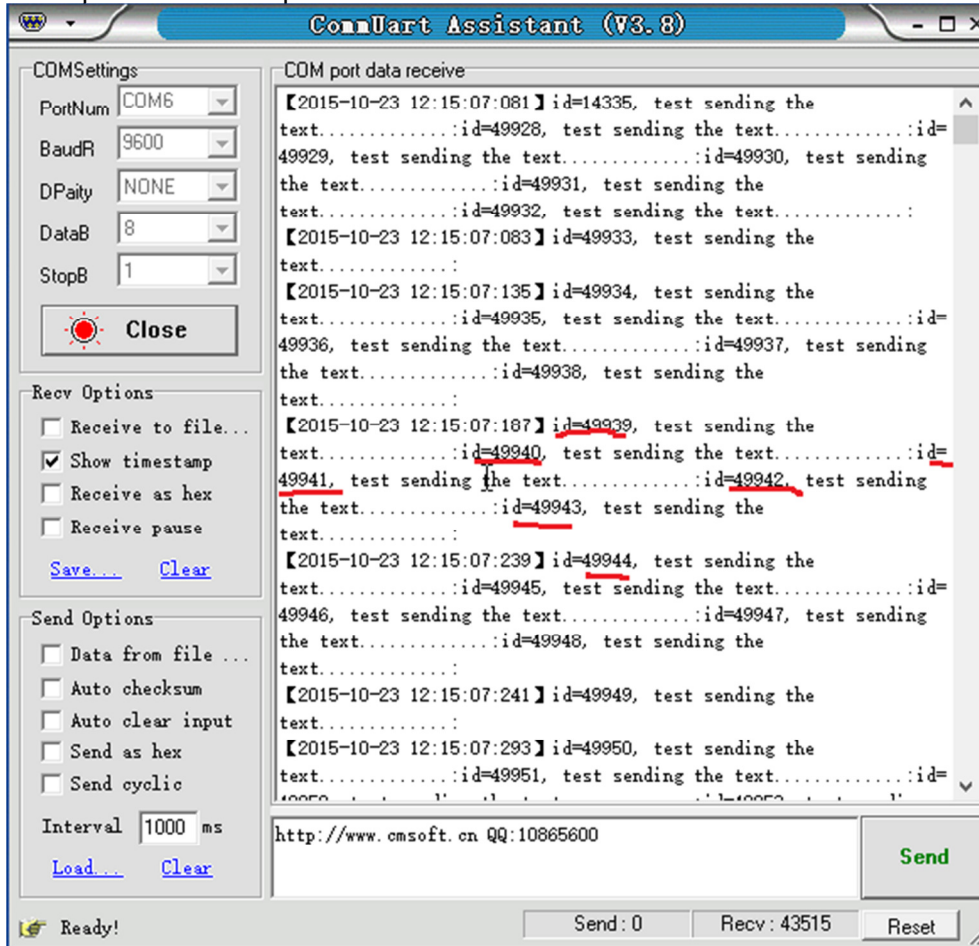
```

79 /* Reset of all peripherals, Initializes the Flash interface and the Systick. */
80 HAL_Init();
81
82 /* Configure the system clock */
83 SystemClock_Config();
84
85 /* Initialize all configured peripherals */
86 MX_GPIO_Init();
87 MX_USB_DEVICE_Init();
88
89 /* USER CODE BEGIN 2 */
90
91 /* USER CODE END 2 */
92
93 /* Infinite loop */
94 /* USER CODE BEGIN WHILE */
95 while (1)
96 {
97     /* USER CODE END WHILE */
98     sprintf(textbuf, "id=%d, test sending the text.....", count++); //export it to char buffer first. In this wa
99     MX_USB_DEVICE_SENT_DATA((uint8_t *)textbuf, strlen(textbuf));
100     HAL_Delay(10);
101     /* USER CODE BEGIN 3 */
102
103 }
104 /* USER CODE END 3 */
105
106 }
107

```

I put a sequence number in the text sent to PC.
Therefore you can check if any data was missing.

A snapshot of the captured data is:



3. Bugs or possible error you may face.

(1). PC doesn't response at all when you plug in the usb cable.

This is the most headache you may face. There are lots of possibility.

One of them is the missing pull-up resistor. Look at the first page. You may need to connect a 1.5kohm pull up resistor.

The idea is the device needs to be correctly identified.

http://www.usbmadesimple.co.uk/ums_3.htm

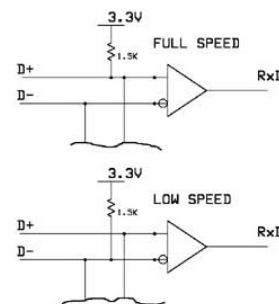
Speed Identification

At the device end of the link a 1.5 kohm resistor pulls one of the lines up to a 3.3V supply derived from VBUS.

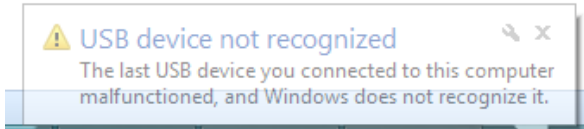
This is on D- for a low speed device, and on D+ for a full speed device.

(A high speed device will initially present itself as a full speed device with the pull-up resistor on D+.)

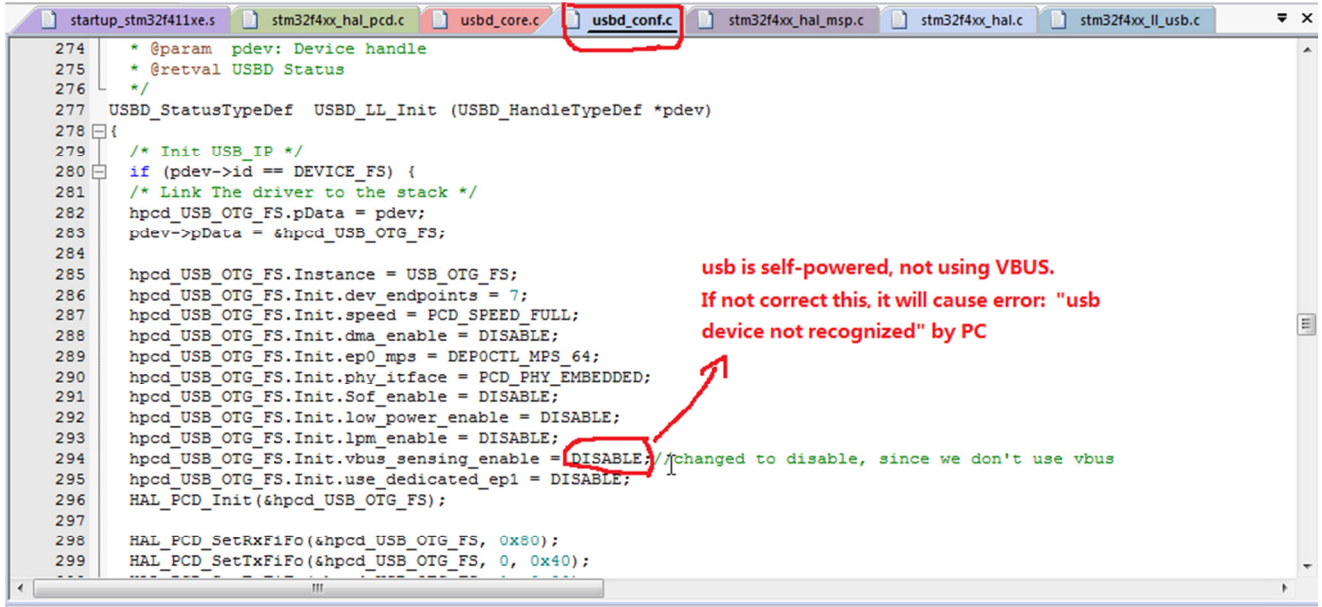
The host can determine the required speed by observing which line is pulled high.



(2). PC response, but it prompts "USB device not recognized"



The problem is that my own board uses self-powered usb. VBus is not used. Therefore it should be disabled.

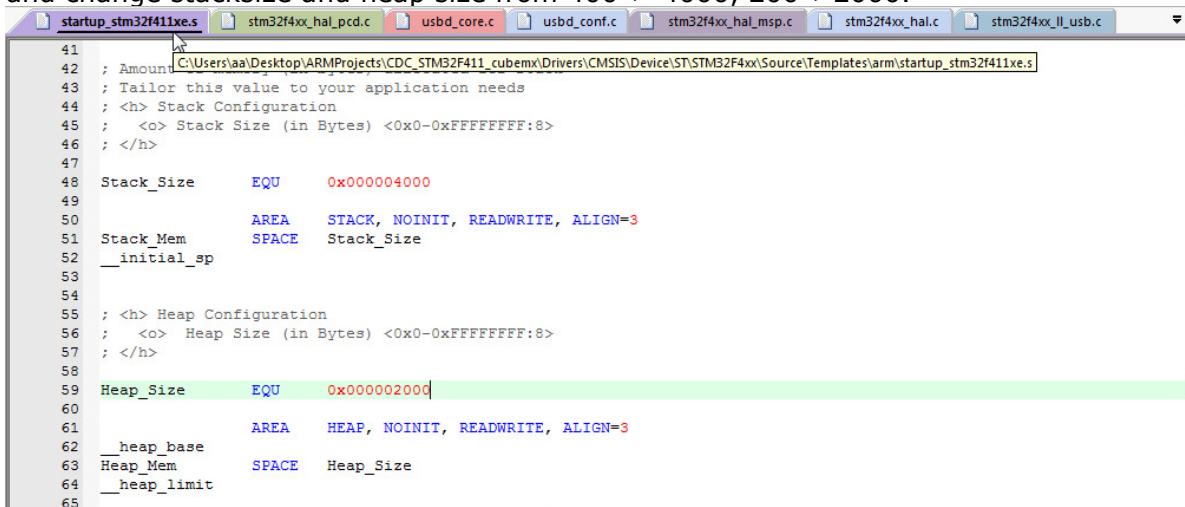


```
274 * @param pdev: Device handle
275 * @retval USB_D Status
276 */
277 USB_D_StatusTypeDef USB_LL_Init (USB_HandleTypeDef *pdev)
278 {
279     /* Init USB IP */
280     if (pdev->id == DEVICE_FS) {
281         /* Link The driver to the stack */
282         hpcd_USB_OTG_FS.pData = pdev;
283         pdev->pData = &hpcd_USB_OTG_FS;
284
285         hpcd_USB_OTG_FS.Instance = USB_OTG_FS;
286         hpcd_USB_OTG_FS.Init.dev_endpoints = 7;
287         hpcd_USB_OTG_FS.Init.speed = PCD_SPEED_FULL;
288         hpcd_USB_OTG_FS.Init.dma_enable = DISABLE;
289         hpcd_USB_OTG_FS.Init.ep0_mps = DEPCTL_MPS_64;
290         hpcd_USB_OTG_FS.Init.phy_iface = PCD_PHY_EMBEDDED;
291         hpcd_USB_OTG_FS.Init.Sof_enable = DISABLE;
292         hpcd_USB_OTG_FS.Init.low_power_enable = DISABLE;
293         hpcd_USB_OTG_FS.Init.lpm_enable = DISABLE;
294         hpcd_USB_OTG_FS.Init.vbus_sensing_enable = DISABLE; /* changed to disable, since we don't use vbus
295         hpcd_USB_OTG_FS.Init.use_dedicated_ep1 = DISABLE;
296         HAL_PCD_Init(&hpcd_USB_OTG_FS);
297
298         HAL_PCD_SetRxFifo(&hpcd_USB_OTG_FS, 0x80);
299         HAL_PCD_SetTxFifo(&hpcd_USB_OTG_FS, 0, 0x40);
...
```

(3).USB is recognized by PC, but reported as unknown or "This device cannot start"

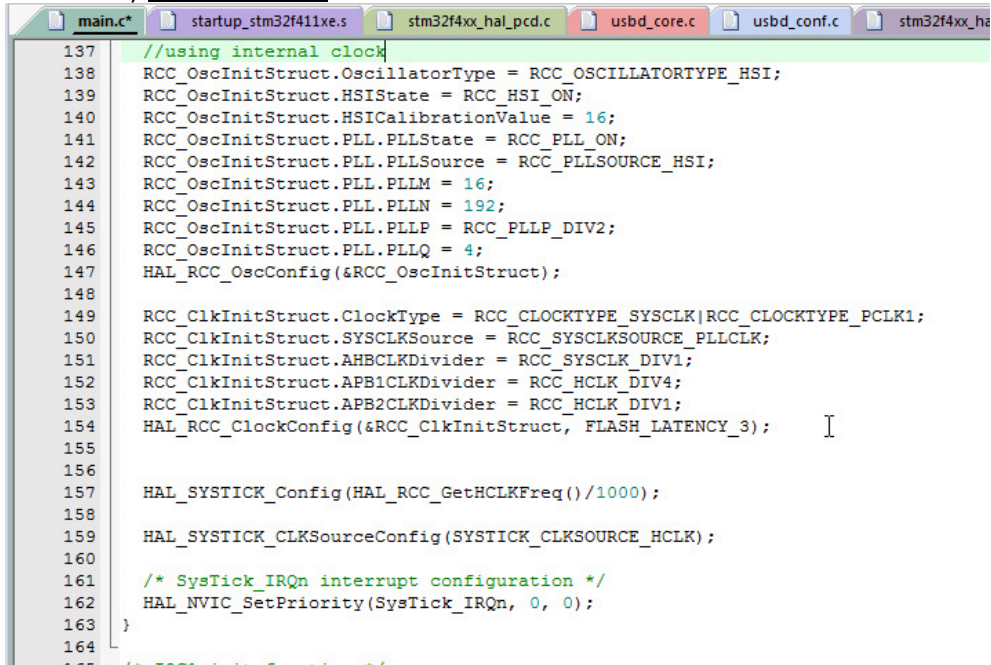
This problem was discussed in the forum as [link](#):

This one is easy to correct. Go to startup file and change stacksize and heap size from 400-> 4000, 200->2000.



```
41
42 ; Amount
43 ; Tailor this value to your application needs
44 ; <h> Stack Configuration
45 ; <o> Stack Size (in Bytes) <0x0-0xFFFFFFFF:8>
46 ; </h>
47
48 Stack_Size EQU 0x000004000
49
50 Stack_Mem AREA STACK, NOINIT, READWRITE, ALIGN=3
51 Stack_Mem SPACE Stack_Size
52 __initial_sp
53
54
55 ; <h> Heap Configuration
56 ; <o> Heap Size (in Bytes) <0x0-0xFFFFFFFF:8>
57 ; </h>
58
59 Heap_Size EQU 0x000002000
60
61 __heap_base AREA HEAP, NOINIT, READWRITE, ALIGN=3
62 Heap_Mem SPACE Heap_Size
63 __heap_limit
64
65
```


Last note, internal clock is OK for the USB data transfer.



```
137 //using internal clock
138 RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSI;
139 RCC_OscInitStruct.HSIState = RCC_HSI_ON;
140 RCC_OscInitStruct.HSICalibrationValue = 16;
141 RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
142 RCC_OscInitStruct.PLL.PLLSource = RCC_PLLSOURCE_HSI;
143 RCC_OscInitStruct.PLL.PLLM = 16;
144 RCC_OscInitStruct.PLL.PLLN = 192;
145 RCC_OscInitStruct.PLL.PLLP = RCC_PLLP_DIV2;
146 RCC_OscInitStruct.PLL.PLLQ = 4;
147 HAL_RCC_OscConfig(&RCC_OscInitStruct);
148
149 RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_SYSCLK|RCC_CLOCKTYPE_PCLK1;
150 RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
151 RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
152 RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV4;
153 RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV1;
154 HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_3);
155
156
157 HAL_SYSTICK_Config(HAL_RCC_GetHCLKFreq()/1000);
158
159 HAL_SYSTICK_CLKSourceConfig(SYSTICK_CLKSOURCE_HCLK);
160
161 /* SysTick_IRQn interrupt configuration */
162 HAL_NVIC_SetPriority(SysTick_IRQn, 0, 0);
163 }
164
```

Dr. RongWei Mao
2015-10-23