

# UM2757

User manual

### Getting started with X-NUCLEO-53L3A2 multi target ranging ToF sensor expansion board based on VL53L3CX for STM32 Nucleo

### Introduction

This document provides detailed hardware information on the X-NUCLEO-53L3A2 expansion board. This expansion board is compatible with the STM32 Nucleo family and the Arduino<sup>™</sup> electronic boards. It is designed around the VL53L3CX ranging sensor and is based on the ST patented FlightSense technology.

To allow the user to validate the VL53L3CX in an environment as close as possible to its final application, the X-NUCLEO-53L3A2 expansion board is delivered with a holder in which three different height spacers of 0.25 mm, 0.5 mm, and 1 mm can be fitted with the cover glass above the spacer. The height spacers are used to simulate different air gap distances between the VL53L3CX sensor and the cover glass.

The X-NUCLEO-53L3A2 expansion board is delivered with two VL53L3CX breakout boards.

### SCL SDA life.augmented CN6 GND S DISPLAY1 Nucleo 3V3 6 6 0000 CN5 GND 00 ........ 6 0 RoHS COMPLIANT X-NUCLEO-53L382 1 CN8 CN9 2V8 P23 VL53L3CX LEFT RIGHT P22 S1 41 00 0 0 0 1 6 函 0 2 Ż XSDN XSDN SCI SCI 1.0 MM GND NCO SC SPI R 0.5MM 0.25MM

### Figure 1. X-NUCLEO-53L3A2 expansion board, spacers, cover glass, and breakout boards

### 1 Overview

The X-NUCLEO-53L3A2 expansion board features the VL53L3CX ranging sensor, based on ST's FlightSense, Time-of-Flight (ToF) technology.

It is compatible with the STM32 Nucleo development board family, and with the Arduino UNO R3 connector layout.

Several ST expansion boards can be stacked through the Arduino connectors, which allows, for example, the development of VL53L3CX applications with Bluetooth or Wi-Fi interfaces.

The X-NUCLEO-53L3A2 expansion board is delivered with:

- Three spacers of 0.25 mm, 0.5 mm, and 1 mm height, used to simulate different air gaps between the VL53L3CX and the cover glass.
- Two cover windows to simulate the integration of the VL53L3CX into the customer's final product.
- Two VL53L3CX breakout boards which can be plugged onto the X-NUCLEO-53L3A2 expansion board or connected through flying wires to the X-NUCLEO-53L3A2 expansion board.
- Two 10-pin connectors to enable the customer to connect the two breakout boards onto the X-NUCLEO-53L3A2 expansion board.

Note: The VL53L3CX is delivered with a liner to prevent potential foreign material from penetrating inside the module holes during the assembly process. This liner must be removed at the latest possible step during final assembly, before module calibration.

### Table 1. Ordering information

| Order code      | Description                                                            |
|-----------------|------------------------------------------------------------------------|
| X-NUCLEO-53L3A2 | STM32 Nucleo expansion board - spacers and glass - two breakout boards |

# 2 Document references

### Table 2. Document references

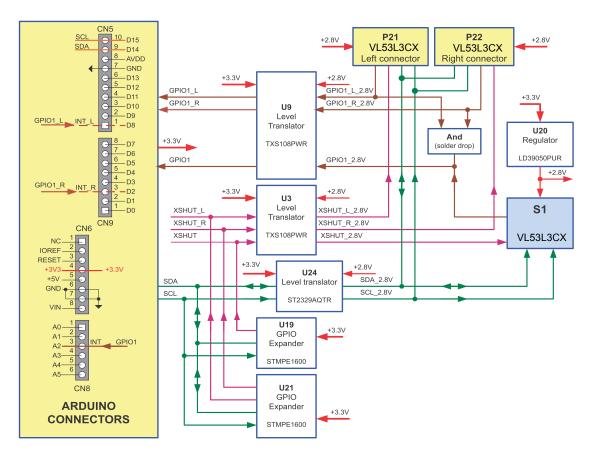
| Description                | DocID   |
|----------------------------|---------|
| VL53L3CX datasheet         | DS13204 |
| X-NUCLEO-53L3A2 data brief | DB4226  |
| P-NUCLEO-53L3A2 data brief | DB4194  |
| X-CUBE-53L3A2 data brief   | DB4193  |



# 3 X-NUCLEO-53L3A2 expansion board

This section describes the X-NUCLEO-53L3A2 expansion board features and provides useful information for understanding the electrical characteristics.





### 3.1 Description

The board allows the user to test the VL53L3CX functionality, to program it and to understand how to develop an application using the VL53L3CX. It integrates:

- 2.8 V regulator to supply the VL53L3CX
- Level translators to adapt the I/O level to the main board of the microcontroller
- Arduino UNO R3 connectors
- Optional VL53L3CX breakout board connectors
- Solder drops to allow different configurations of the expansion board

It is fundamental to program a microcontroller to control the VL53L3CX through the I2C bus. The application software and an example of the C-ANSI source code are available on www.st.com/VL53L3CX.

The X-NUCLEO-53L3A2 expansion board and STM32 Nucleo development board are connected through the Arduino UNO R3 connectors CN5, CN6, CN8, and CN9 as shown in Figure 3. X-NUCLEO-53L3A2 expansion board connector layout and as described in Table 3. Left Arduino connector and Table 4. Right Arduino connector.

The X-NUCLEO-53L3A2 must be plugged onto the STM32 Nucleo development board through the Arduino UNO R3 connectors.

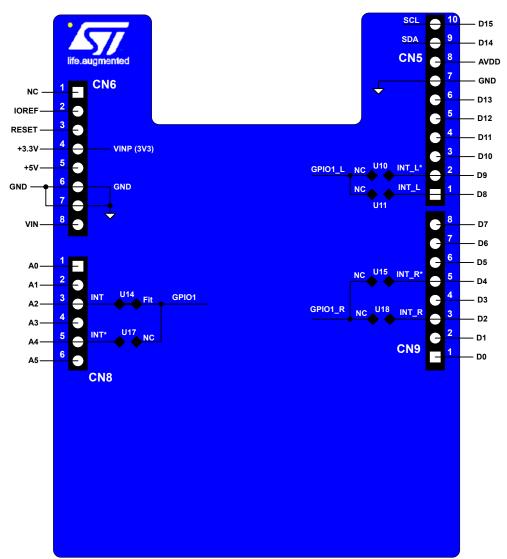


Figure 3. X-NUCLEO-53L3A2 expansion board connector layout

| CN number  | VL53L3CX<br>board | Pin number | Pin name | MCU pin            | X-NUCLEO-53L3A2 expansion<br>board function                                        |  |
|------------|-------------------|------------|----------|--------------------|------------------------------------------------------------------------------------|--|
|            |                   | 1          |          | NC                 |                                                                                    |  |
|            |                   | 2          | NC       | IOREF              | Not used                                                                           |  |
|            |                   | 3          | NC       | RESET              | INUL USED                                                                          |  |
| CN6 power  | Power             | 4          | 3V3      | 3V3                | 3.3 V supply                                                                       |  |
| Civo power |                   | 5          | NC       | 5V                 | Not used                                                                           |  |
|            | Gnd               | 6          | Gnd      | Gnd                | Gnd                                                                                |  |
|            | Gnd               | 7          | Gnd      | Gnd                | Giù                                                                                |  |
|            |                   | 8          | NC       | C VIN              |                                                                                    |  |
|            |                   | 1          | NC       | PAO                | Not used                                                                           |  |
|            |                   | 2          | NC       | PA1                |                                                                                    |  |
|            | GPIO1             | 3          | INT      | PA4                | Interrupt signal from VL53L3CX on<br>board soldered device                         |  |
| CN8 analog |                   | 4          | NC       | PB0                | Not used                                                                           |  |
|            | GPIO1             | 5          | INT      | PC1 <sup>(1)</sup> | By default not used, interrupt<br>signal from VL53L3CX on board<br>soldered device |  |
|            |                   | 6          | NC       | PC0                | Not used                                                                           |  |

#### Table 3. Left Arduino connector

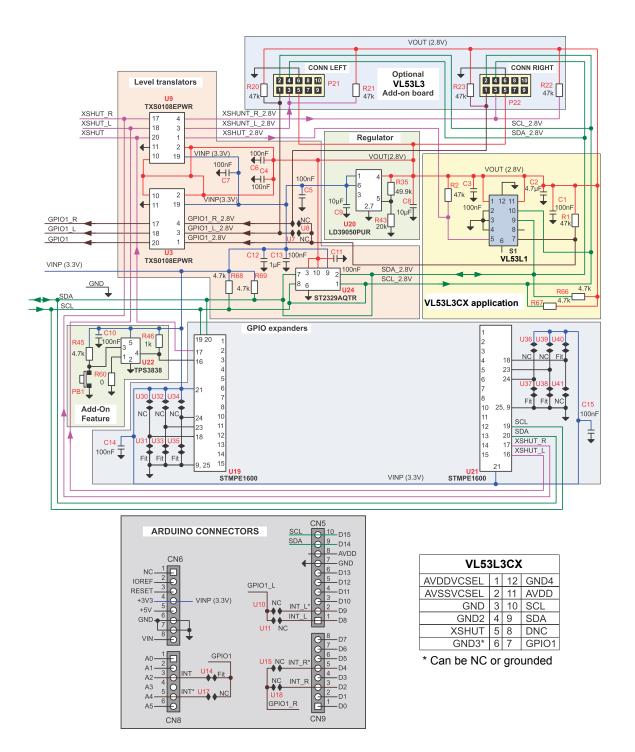
 Depends on STM32 Nucleo board solder bridges, see details in Section 3.3: Solder drop configurations. These interrupt signals are duplicated, but not used. This offers hardware connection flexibility in case of conflict on the MCU interface management when the expansion board is used superimposed with other expansion boards. In this case, remove the solder drop from the used interrupt and instead, fit the solder drop in "NC".

| CN number                                   | VL53L3CX<br>board | Pin number                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | Pin name | MCU pin | X-NUCLEO-53L3A2 expansion<br>board function                                                            |  |
|---------------------------------------------|-------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------|---------|--------------------------------------------------------------------------------------------------------|--|
|                                             | SCL               | 10                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       | D15      | PB8     | I2C1_SCL                                                                                               |  |
| CN number board Pin number Pin name MCO pin | SDA               | 9                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | D14      | PB9     | I2C1_SDA                                                                                               |  |
|                                             |                   | 8                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | NC       | AVDD    | Not used                                                                                               |  |
|                                             | Gnd               | Gnd                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |          |         |                                                                                                        |  |
|                                             | 6                 | INT_L                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | PA5      |         |                                                                                                        |  |
| CN5 digital                                 |                   | boardPin numberPin nameMCO pinboard functionSCL10D15PB8 $12C1\_SCL$ SDA9D14PB9 $12C1\_SDA$ SDA9D14PB9 $12C1\_SDA$ 8NCAVDDNot usedGnd7GndGndGnd6INT_LPA5 $Nctused$ $Notused$ 5NCPA6 $Notused$ $Notused$ 3NCPB6 $Notused$ $Notused$ GPIO1_L2INT_LPC7By default not used, in<br>signal from optional VL53<br>breakout board (1)GPIO1_L1INT_LPA9 $Notused$ 6NCPB4 $Notused$ $Notused$ 5INT_RPB5By default not used, in<br>signal from optional VL53<br>breakout board (1)5INT_RPB5By default not used, in<br>signal from optional VL53<br>right breakout board4NCPB3Not used | Notusod  |         |                                                                                                        |  |
|                                             |                   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          | NC       | PA7     | - Not used                                                                                             |  |
|                                             |                   | 3                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | NC       | PB6     |                                                                                                        |  |
|                                             | GPIO1_L           | 2                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | INT_L    | PC7     | By default not used, interrupt                                                                         |  |
|                                             | GPIO1_L           |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          | INT_L    | PA9     | breakout board <sup>(1)</sup>                                                                          |  |
|                                             |                   | 8                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | NC       | PA8     |                                                                                                        |  |
|                                             |                   | 7                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | NC       | PB10    | Not used                                                                                               |  |
|                                             |                   | 6                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | NC       | PB4     |                                                                                                        |  |
| CN9 digital                                 |                   | 5                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | INT_R    | PB5     | By default not used, interrupt<br>signal from optional VL53L3CX<br>right breakout board <sup>(1)</sup> |  |
|                                             |                   | 4                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | NC       | PB3     | Not used                                                                                               |  |
|                                             |                   | 3                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | INT_R    | PA10    | By default not used, interrupt<br>signal from optional VL53L3CX<br>right breakout board <sup>(1)</sup> |  |
|                                             |                   | 2                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | NC       | PA2     | Not used                                                                                               |  |
|                                             |                   | 1                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | NC       | PA3     | NOL USEU                                                                                               |  |

### Table 4. Right Arduino connector

 These interrupt signals are duplicated, but not used by default. This offers hardware connection of the breakout board VL53L3CX interrupt signals and flexibility in case of conflict on the MCU interface management when the expansion board is used superimposed with other expansion boards. In this case, select, through a solder drop, the MCU port which is free.

#### 3.2 **Electrical schematic**



### Figure 4. X-NUCLEO-53L3A2 expansion board schematic

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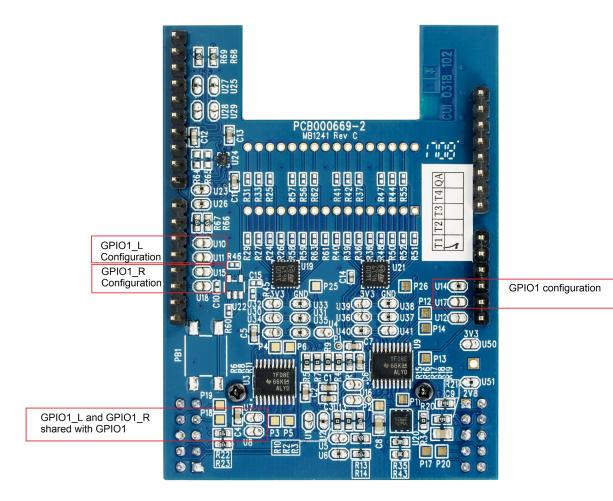
### 3.3 List of materials

|             |                | Table 5. List of I   | liateriais      |                                                             |
|-------------|----------------|----------------------|-----------------|-------------------------------------------------------------|
| Components  | Value          | Reference            | Supplier        | Comments                                                    |
|             |                | VL53L3CX app         | lication        |                                                             |
| C1, C3      | 100 nF         | X5R                  |                 | Ourseland the second second in se                           |
| C2          | 4.7 µF         | X5R - 6.3 V          |                 | Supply voltage decoupling                                   |
| R1          | 47 k           |                      |                 | Interrupt output pull up                                    |
| R2          | 47 k           |                      |                 | Reset input pull up                                         |
| R66, R67    | 4.7 k          |                      |                 | SDA and SCL line pull up at 2.8 V                           |
| S1          |                | VL53L3CX             | ST              | ToF ranging sensor                                          |
|             |                | VL53L3CX breakout be | oard interfaces |                                                             |
| R20         | 47 k           |                      |                 | Left breakout board interrupt output pull up                |
| R21         | 47 k           |                      |                 | Left breakout board reset input pull up                     |
| R22         | 47 k           |                      |                 | Right breakout board reset input pull up                    |
| R23         | 47 k           |                      |                 | Right breakout board interrupt output pull up               |
|             |                | 2.8 V regulator ap   | oplication      |                                                             |
| C8          | 10 µF          | X5R - 6.3 V          |                 | Output voltage decoupling                                   |
| C9          | 10 µF          | X5R - 6.3 V          |                 | Input voltage decoupling                                    |
| R35<br>R43  | 49.9 k<br>20 k |                      |                 | Feedback resistor bridge to set the output voltage to 2.8 V |
| U20         | 20 1           | LD39050PUR           | ST              | Output programmable regulator                               |
|             |                | Level translator a   | pplication      |                                                             |
| C4, C6, C11 | 100 nF         |                      |                 | 2.8 V decoupling capacitor                                  |
| C5, C7, C13 | 100 nF         |                      |                 |                                                             |
| C12         | 1 µF           | X5R - 6.3V           |                 | 3.3 V decoupling capacitor                                  |
| R68, R69    | 4.7 k          |                      |                 | SDA and SCL line pull up at 3.3 V                           |
| U3, U9      |                | TXS0108PWR           | ТІ              | For all signals except I2C interface                        |
| U24         |                | ST2329AQTR           | ST              | For I2C interface                                           |
|             |                | Add-on feat          | ture            |                                                             |
| C10         | 100 nF         |                      |                 | Supply decoupling capacitor                                 |
| R45         | 4.7 k          |                      |                 | Push button pull up                                         |
| R46         | 1 k            |                      |                 | Output pull up                                              |
| R60         |                |                      |                 | Delay time setting (def = 10 ms)                            |
| PB1         |                |                      |                 | Push button                                                 |
| U22         |                | TPS3838K33           | TI              | Supervisory circuit                                         |
|             |                | GPIO expar           | nder            |                                                             |
| C14, C15    | 100 nF         |                      |                 | Supply decoupling capacitor                                 |
|             |                |                      |                 |                                                             |

Table 5. List of materials

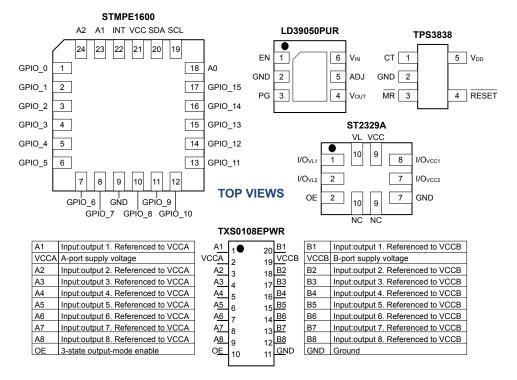
Solder drops allow the following configurations of the X-NUCLEO-53L3A2 expansion board:

- If the developer wants to make an application with several expansion boards stacked and there is:
  - conflict with the microcontroller port allocation, the GPIO1 can be output on the CN8/A4 (U17 fitted) of the Arduino connector. The default configuration is that GPIO1 is output on the CN8/A2 (U14 fitted) of the Arduino connector.
  - conflict on the I2C addresses, the addresses of the STMPE1600 can be modified (the default addresses are A2, A1, A0, 000, and 001).
- If the developer wants to connect breakout boards (see Figure 5. Interrupt configurations) to the X-NUCLEO-53L3A2 expansion board:
  - the VL53L3CX interrupt of the left breakout board can be output on the CN5/D9 (U10 fitted) or CN5/D8 (U11 fitted) of the Arduino connector. By default, the U10 and U11 are not fitted.
  - the VL53L3CX interrupt of the right breakout board can be output on the CN9/D4 (U15 fitted) or CN9/D2 (U18 fitted) of the Arduino connector. By default, the U15 and U18 are not fitted.
  - the VL53L3CX interrupt of the left and right breakout boards, GPIO1\_L and GPIO1\_R, can be shared with the VL53L3CX interrupt on the main board, GPIO1, by fitting U7 and U8 solder drops. By default U7 and U8 are not fitted.



### Figure 5. Interrupt configurations

### 3.5 Integrated device pinning



#### Figure 6. Integrated device pinning

## 4 VL53L3CX breakout board

The VL53L3CX breakout boards are supplied at 2.8 V by the regulator present on the X-NUCLEO-53L3A2 expansion board.

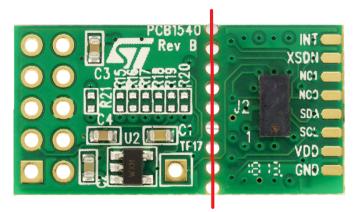
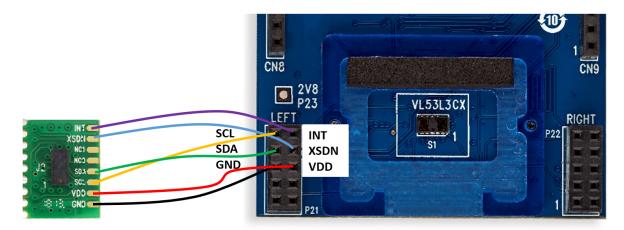


Figure 7. VL53L3CX breakout board schematic

The VL53L3CX breakout boards can be directly plugged onto the X-NUCLEO-53L3A2 expansion board through the two 10-pin connectors or connected to the board through flying leads.

When connected through flying leads, developers should break off the mini PCB from the breakout board, and use only the "VL53L3CX mini PCB" which because of its small size, is easier to integrate into customer devices.



#### Figure 8. VL53L3CX mini PCB flying lead connection to X-NUCLEO-53L3A2 expansion board

### 5 Safety considerations

### 5.1 Electrostatic precaution

The user should exercise electrostatic precautions, including using ground straps when using the X-NUCLEO-53L3A2 expansion board. Failure to prevent electrostatic discharge could damage the device.

#### Figure 9. Electrostatic logo



### 5.2 Laser safety considerations

The VL53L3CX contains a laser emitter and corresponding drive circuitry. The laser output is designed to remain within Class 1 laser safety limits under all reasonably foreseeable conditions including single faults, in compliance with the IEC 60825-1:2014 (third edition). The laser output remains within Class 1 limits as long as STMicroelectronic's recommended device settings are used and the operating conditions specified in the datasheet are respected. The laser output power must not be increased by any means and no optics should be used with the intention of focusing the laser beam.

Figure 10. Class 1 laser product label



# **Revision history**

### Table 6. Document revision history

| Date        | Version | Changes         |
|-------------|---------|-----------------|
| 10-Sep-2020 | 1       | Initial release |

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