

Figure 51. SAI GPIO and pin configuration

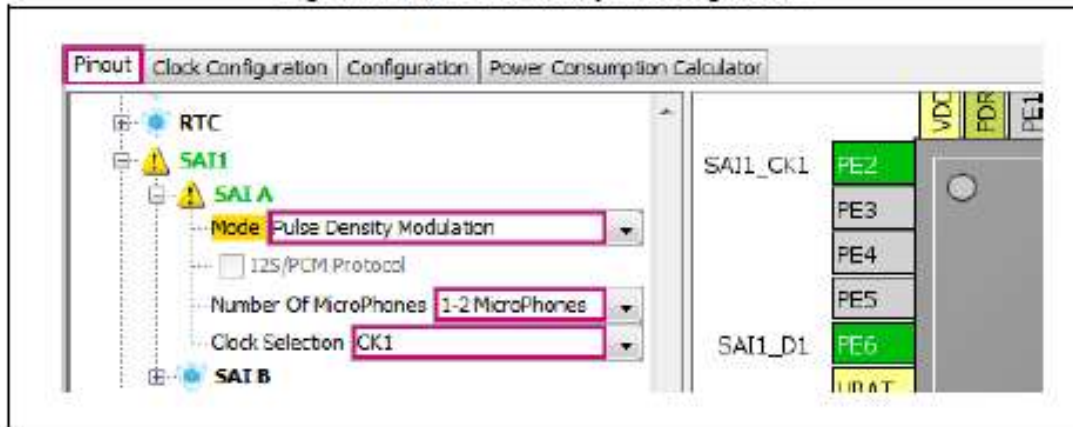


Figure 54. SAI parameter settings

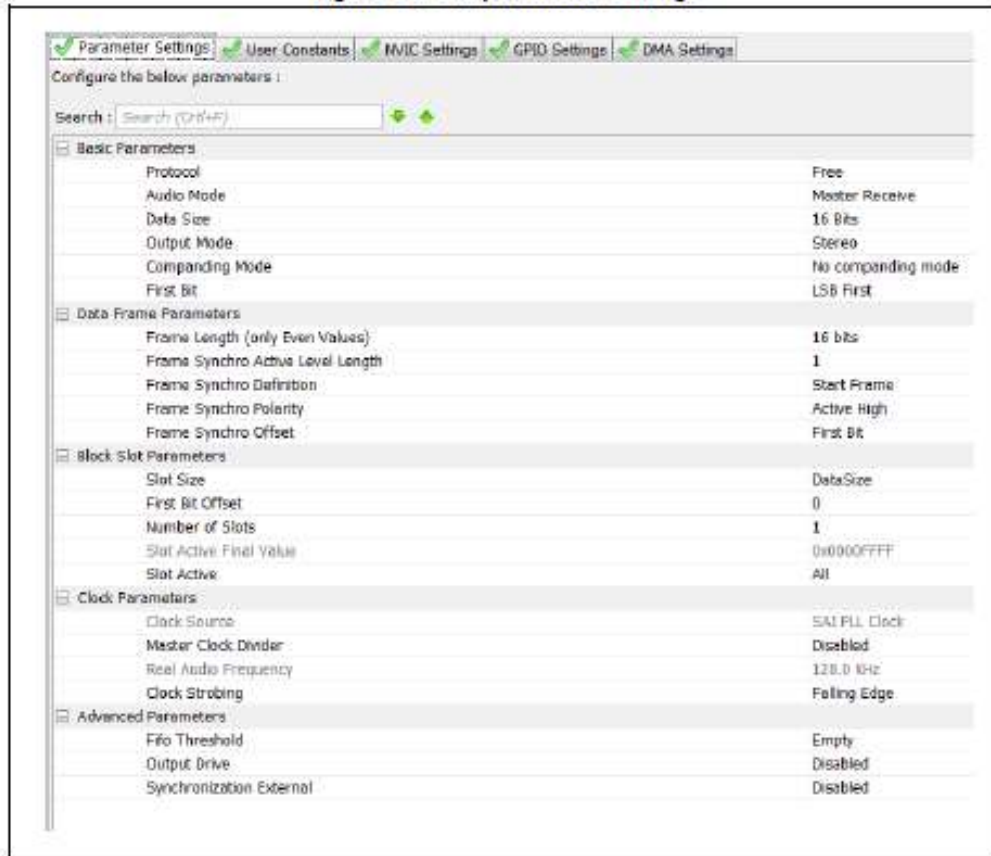
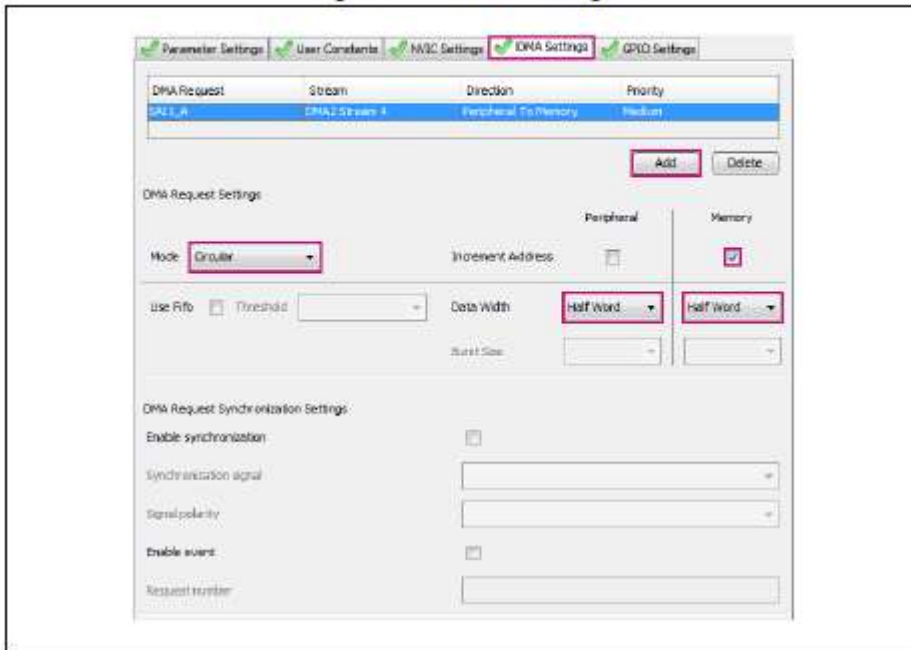


Figure 55. SAI DMA settings



### Cortex\_M7 configuration

Select the configuration tab then click on the CORTEX\_M7 button as shown in [Figure 56](#).

Figure 56. CORTEX\_M7 configuration

